

# Aritra Banerjee

## List of Publications by Year in descending order

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Version: 2024-02-01

30  
papers

244  
citations

1307594

7  
h-index

1372567

10  
g-index

30  
all docs

30  
docs citations

30  
times ranked

204  
citing authors

#	ARTICLE	IF	CITATIONS
1	A Stacked Segmented Adaptive Power Amplifier in 22nm FD-SOI. IEEE Microwave and Wireless Components Letters, 2022, 32, 983-986.	3.2	1
2	A High Efficiency Multi-Mode Outphasing RF Power Amplifier With 31.6 dBm Peak Output Power in 45nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 815-828.	5.4	14
3	Millimeter-Wave Transceivers for Wireless Communication, Radar, and Sensing : (Invited Paper). , 2019, , .		10
4	Sub-THz and THz Signal Generation Using Photonic and Electronic Techniques (Invited Paper). , 2019, , .		0
5	A 29.5 dBm Class-E Outphasing RF Power Amplifier With Efficiency and Output Power Enhancement Circuits in 45nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 1977-1988.	5.4	17
6	Digitally Assisted Built-In Tuning Using Hamming Distance Proportional Signatures in RF Circuits. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 2918-2931.	3.1	2
7	Digitally-compatible ring oscillator frequency driven tuning of CN-TFT amplifiers: Performance compensation under statistical and morphological variations. , 2015, , .		1
8	High efficiency multi-mode outphasing RF power amplifier in 45nm CMOS. , 2015, , .		10
9	Efficiency improvement techniques for RF power amplifiers in deep submicron CMOS. , 2015, , .		4
10	A PWM Based Fully Integrated Digital Transmitter/PA for WLAN and LTE Applications. IEEE Journal of Solid-State Circuits, 2015, 50, 1117-1125.	5.4	17
11	A 25 dBm Outphasing Power Amplifier With Cross-Bridge Combiners. IEEE Journal of Solid-State Circuits, 2015, 50, 1107-1116.	5.4	27
12	Automatic Test Stimulus Generation for Diagnosis of RF Transceivers Using Model Parameter Estimation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 3114-3118.	3.1	3
13	Signature Driven Hierarchical Post-Manufacture Tuning of RF Systems for Performance and Power. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 342-355.	3.1	8
14	Low cost implicit built-in self-test of passive RFID Tags. , 2014, , .		1
15	Yield Recovery of RF Transceiver Systems Using Iterative Tuning-Driven Power Conscious Performance Optimization. IEEE Design and Test, 2014, , 1-1.	1.2	12
16	Atomic model learning: A machine learning paradigm for post silicon debug of RF/analog circuits. , 2014, , .		7
17	A 29.5 dBm class-E outphasing RF power amplifier with performance enhancement circuits in 45nm CMOS. , 2014, , .		4
18	Low Cost Signal Reconstruction Based Testing of RF Components using Incoherent Undersampling. Journal of Electronic Testing: Theory and Applications (JETTA), 2014, 30, 213-228.	1.2	0

#	ARTICLE	IF	CITATIONS
19	Adaptive RF Front-end Design via Self-discovery: Using Real-time Data to Optimize Adaptation Control. , 2013, , .		5
20	An adaptive class-E power amplifier with improvement in efficiency, reliability and process variation tolerance. , 2013, , .		4
21	Low cost signal reconstruction based testing of RF components using incoherent undersampling. , 2013, , .		3
22	VAST: Post-Silicon VALidation and Diagnosis of RF/Mixed-Signal Circuits Using Signature Tests. , 2013, , .		6
23	Optimal Testing of Digitally Assisted Adaptive RF Systems. , 2012, , .		0
24	Testing of digitally assisted adaptive analog/RF systems using tuning knob &#x2014; Performance space estimation. , 2012, , .		1
25	Automatic test stimulus generation for accurate diagnosis of RF systems using transient response signatures. , 2011, , .		10
26	Accurate signature driven power conscious tuning of RF systems using hierarchical performance models. , 2011, , .		10
27	Power Aware Post-manufacture Tuning of Analog Nanocircuits. , 2011, , .		3
28	Optimized Multitone Test Stimulus Driven Diagnosis of RF Transceivers Using Model Parameter Estimation. , 2011, , .		24
29	Optimized digital compatible pulse sequences for testing of RF front end modules. , 2010, , .		6
30	Analog Signature- Driven Postmanufacture Multidimensional Tuning of RF Systems. IEEE Design and Test of Computers, 2010, 27, 6-17.	1.0	34