Antonio J LÃ³pez MartÃn

List of Publications by Year
in descending order

256 papers	4,030 citations	186265 28 h-index	182427 51 g-index
256 all docs	256 docs citations	256 times ranked	1415 citing authors

#	Article	IF	Citations
1	Monitoring of Electric Buses Within an Urban Smart City Environment. IEEE Sensors Journal, 2022, 22, 11364-11372.	4.7	10
2	Fault Detection of Planetary Gears Based on Signal Space Constellations. Sensors, 2022, 22, 366.	3.8	3
3	±0.3V Bulk-Driven Fully Differential Buffer with High Figures of Merit. Journal of Low Power Electronics and Applications, 2022, 12, 35.	2.0	4
4	Super-Gain-Boosted AB-AB Fully Differential Miller Op-Amp With 156dB Open-Loop Gain and 174MV/V MHZ pF/µW Figure of Merit in 130nm CMOS Technology. IEEE Access, 2021, 9, 57603-57617.	4.2	9
5	An Enhanced Gain-Bandwidth Class-AB Miller op-amp With 23,800 MHz·pF/mW FOM, 11-16 Current Efficiency and Wide Range of Resistive and Capacitive Loads Driving Capability. IEEE Access, 2021, 9, 69783-69797.	4.2	5
6	Energy-Efficient Amplifiers Based on Quasi-Floating Gate Techniques. Applied Sciences (Switzerland), 2021, 11, 3271.	2.5	2
7	A family of AC amplifiers for ultra-low frequency operation. International Journal of Circuit Theory and Applications, 2021, 49, 3317-3327.	2.0	3
8	Gain-Boosted Super Class AB OTAs Based on Nested Local Feedback. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 3562-3573.	5.4	9
9	AC amplifiers with ultra-low corner frequency by using bootstrapping. Electronics Letters, 2021, 57, 203-205.	1.0	3
10	Energy-Efficient Symmetrical Cascode OTA in a 130 nm CMOS Process. , 2021, , .		0
11	A 1.2-V Current-Mode RMS-to-DC Converter Based on a Novel Two-Quadrant Electronically Simulated MOS Translinear Loop. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 420-424.	3.0	5
12	Guest Editorial: Special issue on low voltage low power integrated circuits and systems. Microelectronics Journal, 2020, 95, 104674.	2.0	0
13	1-V 15-µW 130-nm CMOS Super Class AB OTA. , 2020, , .		3
14	Power Efficient Simple Technique to Convert a Reset-and-Hold Into a True-Sample-and-Hold Using an Auxiliary Output Stage. IEEE Access, 2020, 8, 66508-66516.	4.2	3
15	Low-Voltage 0.81mW, 1–32 CMOS VGA With 5% Bandwidth Variations and −38dB DC Rejection. IEEE Access, 2020, 8, 106310-106321.	4.2	6
16	360 nW Gate-Driven Ultra-Low Voltage CMOS Linear Transconductor With 1 MHz Bandwidth and Wide Input Range. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 2332-2336.	3.0	13
17	Subsampling OFDM-Based Ultrasonic Data Communication Through Metallic Channels for Monitoring of Cargo Containers. IEEE Transactions on Intelligent Transportation Systems, 2019, 20, 4245-4250.	8.0	1
18	Pseudo-Three-Stage Miller Op-Amp With Enhanced Small-Signal and Large-Signal Performance. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 2246-2259.	3.1	14

#	Article	IF	Citations
19	Class AB amplifier with enhanced slew rate and GBW. International Journal of Circuit Theory and Applications, 2019, 47, 1199-1210.	2.0	20
20	CMOS First-Order All-Pass Filter With 2-Hz Pole Frequency. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 294-303.	3.1	8
21	Â±0.25-V Class-AB CMOS Capacitance Multiplier and Precision Rectifiers. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 830-842.	3.1	8
22	An Op-Amp Approach for Bandpass VGAs With Constant Bandwidth. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1144-1148.	3.0	5
23	An Amplified Offset Compensation Scheme and Its Application in a Track and Hold Circuit. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 416-420.	3.0	11
24	Enhanced Single-Stage Folded Cascode OTA Suitable for Large Capacitive Loads. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 441-445.	3.0	33
25	Â±0.18â€V supply voltage gateâ€driven PGA with 0.7â€Hz to 2â€kHz constant bandwidth and 0.15â€Î¼W power dissipation. International Journal of Circuit Theory and Applications, 2018, 46, 272-279.	2.0	9
26	Â±0.5 V 15 <inline-formula> <tex-math notation="LaTeX">mu </tex-math> </inline-formula>W Recycling Folded Cascode Amplifier With 34767 MHzÂ·pF/mA FOM. IEEE Solid-State Circuits Letters, 2018, 1, 170-173.	2.0	18
27	Super class AB RFC OTA using nonâ€linear current mirrors. Electronics Letters, 2018, 54, 1317-1318.	1.0	10
28	Super class AB RFC OTA with adaptive local commonâ€mode feedback. Electronics Letters, 2018, 54, 1272-1274.	1.0	12
29	Ultra-Low Power Subthreshold Quasi Floating Gate CMOS Logic Family for Energy Harvesting. , 2018, , .		4
30	A Highly Efficient Composite Class-ABâ€"AB Miller Op-Amp With High Gain and Stable From 15 pF Up To Very Large Capacitive Loads. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2061-2072.	3.1	31
31	Super Class-AB Recycling Folded Cascode OTA. IEEE Journal of Solid-State Circuits, 2018, 53, 2614-2623.	5.4	65
32	Folded Cascode OTA with 5540 MHzpF/mA FoM. , 2018, , .		5
33	Modular Discrete and CMOS Integrated Implementations of High-Speed Analog Rank-Order Filters. Circuits, Systems, and Signal Processing, 2018, 37, 5637-5646.	2.0	0
34	Powerâ€efficient classâ€AB telescopic cascode opamp. Electronics Letters, 2018, 54, 620-622.	1.0	17
35	Evaluating engineering competencies in curricular internships. , 2018, , .		1
36	On the Optimal Current Followers for Wide-Swing Current-Efficient Amplifiers. , 2018, , .		4

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37	Super class AB OTA without openâ€loop gain degradation based on dynamic cascode biasing. International Journal of Circuit Theory and Applications, 2017, 45, 2111-2118.	2.0	24
38	Class AB differential difference amplifier for enhanced commonâ€mode feedback. Electronics Letters, 2017, 53, 454-456.	1.0	8
39	Enhanced differential super class-AB OTA. , 2017, , .		0
40	Improved common-mode feedback based on LCMFB techniques. , 2017, , .		4
41	A Simple Miller Compensation With Essential Bandwidth Improvement. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 3186-3192.	3.1	3
42	Powerâ€efficient class AB fully differential amplifier. Electronics Letters, 2017, 53, 1298-1300.	1.0	16
43	New organizational and assessment frameworks for company internship programs. , 2017, , .		2
44	CMOS analogue current-mode multiplier/divider circuit operating in triode-saturation with bulk-driven techniques. The Integration VLSI Journal, 2017, 59, 243-246.	2.1	17
45	Low-Power Analog Channel Selection Filtering Techniques. Circuits, Systems, and Signal Processing, 2017, 36, 895-915.	2.0	1
46	A super class-AB OTA with high output current and no open loop gain degradation. , 2017, , .		3
47	Super class AB transconductor with slew-rate enhancement using QFG MOS techniques. , 2017, , .		7
48	Offset compensation in a track and hold circuit. , 2017, , .		0
49	High current efficiency class-AB OTA with high open loop gain and enhanced bandwidth. IEICE Electronics Express, 2017, 14, 20170719-20170719.	0.8	13
50	Smart ecosystem for a sustainable, safe and integrated freight transport. , 2016, , .		2
51	Design of flexible cost-efficient international engineering curricula at Public University of Navarre. , 2015, , .		2
52	Balanced G_mâ€€ filters with improved linearity and power efficiency. International Journal of Circuit Theory and Applications, 2015, 43, 1147-1166.	2.0	6
53	Industrial integrated circuit design techniques. , 2015, , .		0
54	Highly accurate CMOS second generation current conveyor and transconductor. , 2015, , .		2

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55	Redefining best practices in company internships. , 2015, , .		0
56	Energy harvesting microsystems based on the QFG MOS transistors. , 2015, , .		4
57	Free class AB–AB Miller opamp with high current enhancement. Electronics Letters, 2015, 51, 215-217.	1.0	16
58	Slew rate enhancement based on use of squaring circuits. Electronics Letters, 2015, 51, 219-220.	1.0	8
59	Low-power CMOS variable gain amplifier based on a novel tunable transconductor. IET Circuits, Devices and Systems, 2015, 9, 105-110.	1.4	18
60	Constant g_m rail-to-rail CMOS OpAmp with only one differential pair and switched level shifters. , 2015, , .		1
61	Class AB two stage and folded cascode OpAmps based on a squaring circuit. , 2015, , .		2
62	Design of CMOS amplifiers with offset rejection using positive-feedback QFG transistors. Analog Integrated Circuits and Signal Processing, 2015, 85, 217-221.	1.4	2
63	Turning a basic electronics lab into a low-cost communication systems lab. , 2015, , .		1
64	CMOS class-AB tunable voltage-feedback current operational amplifier. , 2014, , .		2
65	Novel automatic digital calibration techniques for GMR sensors. , 2014, , .		1
66	Highly linear micropower class AB current mirrors using Quasi-Floating Gate transistors. Microelectronics Journal, 2014, 45, 1261-1267.	2.0	14
67	Power Efficient Class AB Op-Amps With High and Symmetrical Slew Rate. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 943-947.	3.1	24
68	Highly linear wide-swing continuous tuning of CMOS transconductors. International Journal of Circuit Theory and Applications, 2014, 42, 831-841.	2.0	6
69	Self-Biased Dual-Path Push-Pull Output Buffer Amplifier for LCD Column Drivers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 663-670.	5.4	27
70	University-industry collaboration chairs: Initiatives at the Public University of Navarre. , 2014, , .		7
71	The influence of gender in the adoption of engineering studies. , 2014, , .		0
72	The role of university-industry liaisons in achieving comprehensive curricula in engineering. , 2014, , .		2

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73	Fostering Industry-Academia synergies in the curricular development of engineering education. , 2014, , .		1
74	Low-Power Class-AB CMOS Voltage Feedback Current Operational Amplifier With Tunable Gain and Bandwidth. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 574-578.	3.0	15
75	CMOS op-amps for biomedical applications. , 2014, , .		2
76	Low power analog front-end electronics in deep submicrometer CMOS technology based on gain enhancement techniques. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2014, 749, 90-95.	1.6	1
77	Engineering international programs at the public university of Navarre: A satisfactory on-going experience in a context of industrial globalization. , 2014, , .		1
78	The role of university-industry liaisons to enhance engineering curricular development. , 2014, , .		1
79	Improved technique for continuous tuning of CMOS transconductor. , 2013, , .		2
80	Energy-efficient class AB CMOS Sample and Hold circuit. , 2013, , .		0
81	Design of micropower class AB transconductors: A systematic approach. Microelectronics Journal, 2013, 44, 920-929.	2.0	11
82	Contactless potentiometers for automotive applications. , 2013, , .		0
83	Low-cost analog interface circuit for resistive bridge sensors. , 2013, , .		7
84	Interactive didactical tools for e-learning in a communication systems course. , 2013, , .		1
85	Engineering outreach programs at the Public University of Navarre: A holistic approach. , 2013, , .		6
86	Sensor signal linearization techniques: A comparative analysis. , 2013, , .		12
87	Tunable Class AB CMOS Gm-C Filter Based on Quasi-Floating Gate Techniques. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 1300-1309.	5.4	40
88	An ultra low energy 8-bit charge redistribution ADC for wireless sensors. , 2013, , .		3
89	Micropower Class-AB VGA With Gain-Independent Bandwidth. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 397-401.	3.0	21
90	A Very Linear Low-Pass Filter with Automatic Frequency Tuning. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 182-187.	3.1	14

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91	On the optimal choice of the output stage in CMOS transconductors. , 2013, , .		1
92	The Flipped Voltage Follower: Theory and Applications. Lecture Notes in Electrical Engineering, 2013, , 269-287.	0.4	9
93	Low-voltage highly-linear class AB current mirror with dynamic cascode biasing. Electronics Letters, 2012, 48, 1336.	1.0	13
94	High-performance micropower class AB current mirror. Electronics Letters, 2012, 48, 823.	1.0	11
95	Role of group evaluation and autoevaluation methods within the learnign process in engineering. , 2012, , .		0
96	Micropower class AB voltage followers with simple quiescent current control. , 2012, , .		5
97	Three novel improved CMOS Câ€multipliers. International Journal of Circuit Theory and Applications, 2012, 40, 607-616.	2.0	27
98	Powerâ€efficient analog design based on the class AB super source follower. International Journal of Circuit Theory and Applications, 2012, 40, 1143-1163.	2.0	32
99	A novel CMOS triode transconductor based on current division. Analog Integrated Circuits and Signal Processing, 2012, 70, 113-121.	1.4	2
100	Using Floating Gate and Quasi-Floating Gate Techniques for Rail-to-Rail Tunable CMOS Transconductor Design. IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 1604-1614.	5.4	57
101	Accurate micropower class AB CMOS voltage-to-current converter. , 2011, , .		4
102	Design of Two-Stage Class AB CMOS Buffers: A Systematic Approach. ETRI Journal, 2011, 33, 393-400.	2.0	8
103	Fully Differential Current-Mode CMOS Triode Translinear Multiplier. IEEE Transactions on Circuits and Systems II: Express Briefs, 2011, 58, 21-25.	3.0	13
104	Current-mode CMOS multiplier/divider circuit operating in linear/saturation regions. Analog Integrated Circuits and Signal Processing, 2011, 66, 299-302.	1.4	12
105	Micropower high currentâ€drive class AB CMOS currentâ€feedback operational amplifier. International Journal of Circuit Theory and Applications, 2011, 39, 893-903.	2.0	17
106	A CMOS QFG ECG amplifier with baseline stabilization. , 2010, , .		1
107	CMOS operational amplifiers with continuous-time capacitive common mode feedback. , 2010, , .		7
108	Micropower active-RC channel filter for a zero-IF Bluetooth receiver. Analog Integrated Circuits and Signal Processing, 2010, 63, 1-8.	1.4	1

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109	DC offset control with application in a zero-IF 0.18ÂÎ¼m CMOS Bluetooth receiver chain. Analog Integrated Circuits and Signal Processing, 2010, 65, 15-20.	1.4	3
110	Attracting Prospective Engineering Students in the Emerging European Space for Higher Education. IEEE Transactions on Education, 2010, 53, 46-52.	2.4	27
111	Three novel improved CMOS capacitance scaling schemes. , 2010, , .		10
112	Tunable rail-to-rail FGMOS transconductor. , 2010, , .		4
113	Compact low-voltage CMOS current-mode multiplier/divider. , 2010, , .		15
114	Class AB CMOS tunable transconductor. , 2010, , .		3
115	CMOS triode transconductor based on quasi-floating-gate transistors. Electronics Letters, 2010, 46, 1190.	1.0	15
116	Simple low voltage, low power implementations of circuits for V<inf>T</inf> extraction. , 2010, , .		1
117	Low-voltage g<inf>m</inf>-enhanced CMOS differential pairs using positive feedback. , 2010, , .		6
118	A low-voltage, high linear programmable triode transconductor. , 2010, , .		3
119	200 μW CMOS class AB unity-gain buffers with accurate quiescent current control. , 2010, , .		3
120	Low-Voltage Tunable Pseudo-Differential Transconductor with High Linearity. ETRI Journal, 2009, 31, 576-584.	2.0	6
121	Performance tradeoffs of integrated CMOS charge amplifiers. , 2009, , .		1
122	A new scheme for DC offset compensation and its application to current mode and voltage mode D/A converters. , 2009, , .		0
123	Programmable capacitance scaling scheme based on operational transconductance amplifiers. Electronics Letters, 2009, 45, 159.	1.0	27
124	Low-power baseband filter for zero-intermediate frequency digital video broadcasting terrestrial/handheld receivers. IET Circuits, Devices and Systems, 2009, 3, 291-301.	1.4	8
125	Versatile multi-decade CMOS voltage-controlled oscillator with accurate amplitude and pulse width control. Analog Integrated Circuits and Signal Processing, 2009, 60, 83-92.	1.4	4
126	Low-Voltage MOS Translinear Analog Signal Processing. Circuits, Systems, and Signal Processing, 2009, 28, 795-804.	2.0	3

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127	A tunable highly linear CMOS transconductor with 80 dB of SFDR. The Integration VLSI Journal, 2009, 42, 277-285.	2.1	10
128	Tunable Linear MOS Resistors Using Quasi-Floating-Gate Techniques. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 41-45.	3.0	30
129	Highly Linear Tunable CMOS $Gm{\hbox{-}}C$ Low-Pass Filter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 2145-2158.	5.4	60
130	Low-voltage first-order fully differential CMOS all-pass filter with programmable pole-zero. Electronics Letters, 2009, 45, 385.	1.0	13
131	Performance Tradeoffs of Three Novel GMR Contactless Angle Detectors. IEEE Sensors Journal, 2009, 9, 191-198.	4.7	32
132	Low-voltage CMOS cross-quad implementation based on dynamic positive feedback. , 2009, , .		0
133	Micropower class AB CMOS current conveyor based on quasi-floating gate techniques. , 2009, , .		4
134	Power-efficient class AB CMOS buffer. Electronics Letters, 2009, 45, 89.	1.0	28
135	Techniques for the Design of Low Voltage Power Efficient Analog and Mixed Signal Circuits. , 2009, , .		4
136	Low-Voltage Power-Efficient Amplifiers for Emerging Applications. , 2009, , 147-165.		2
137	A power efficient and simple scheme for dynamically biasing cascode amplifiers and telescopic op-amps. The Integration VLSI Journal, 2008, 41, 539-543.	2.1	3
138	Two-Stage Differential Charge and Transresistance Amplifiers. IEEE Transactions on Instrumentation and Measurement, 2008, 57, 309-320.	4.7	25
139	CMOS linear programmable transconductor suitable for adjustable Gm-C filters. Electronics Letters, 2008, 44, 505.	1.0	15
140	Low Voltage Differential Input Stage With Improved CMRR and True Rail-to-Rail Common Mode Input Range. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 1229-1233.	3.0	10
141	A CMOS linear tunable transconductor for continuous-time tunable Gm-C filters. , 2008, , .		7
142	High slew rate two stage A/AB and AB/AB op-amps with phase lead compensation at output node and local common mode feedback. , 2008, , .		4
143	Rail-to-rail fully differential sample and hold based on differential difference amplifier. Electronics Letters, 2008, 44, 656.	1.0	9
144	Linear-enhanced V to I converters based on MOS resistive source degeneration. , 2008, , .		3

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145	A simple approach for the implementation of CMOS amplifiers with constant bandwidth independent of gain. , 2008, , .		17
146	A 1.2-V 140-nW 10-bit Sigmaâ€"Delta Modulator for Electroencephalogram Applications. IEEE Transactions on Biomedical Circuits and Systems, 2008, 2, 223-230.	4.0	28
147	CMOS Transconductors With Continuous Tuning Using FGMOS Balanced Output Current Scaling. IEEE Journal of Solid-State Circuits, 2008, 43, 1313-1323.	5.4	49
148	A Tunable Pseudo-Differential OTA With $-78~{hbox {dB}}$ THD Consuming 1.25 mW. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 527-531.	3.0	18
149	Loop filter approximations for PLLs. , 2008, , .		5
150	An Input Stage for the Implementation of Low-Voltage Rail to Rail Offset Compensated CMOS Comparators. , 2008, , .		0
151	Class-AB Fully Differential Voltage Followers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 131-135.	3.0	5
152	Design of highly linear multipliers using floating gate transistors and/or source degeneration resistor. , 2008, , .		2
153	Compact class AB CMOS current mirror. Electronics Letters, 2008, 44, 1335.	1.0	27
154	A âˆ'72 dB @ 2 MHz IM3 CMOS tunable pseudo-differential transconductor. , 2008, , .		4
155	Comparison of programmable linear resistors based on quasi-floating gate MOSFETs. , 2008, , .		7
156	Recent Developments in Electronic Coin Detectors. Recent Patents on Electrical Engineering, 2008, 1, 146-154.	0.4	1
157	A Very Linear OTA with V-I Conversion based on Quasi-Floating MOS Resistor. , 2007, , .		5
158	Sensing in Coin Discriminators. , 2007, , .		16
159	Class AB CMOS analogue squarer circuit. Electronics Letters, 2007, 43, 1059.	1.0	12
160	Class AB fully differential voltage followers. , 2007, , .		0
161	Low-Voltage Universal Capacitive Threshold Logic gate and its application in m-out-of-n functions. Midwest Symposium on Circuits and Systems, 2007, , .	1.0	0
162	Performance Tradeoffs of Three Contactless Angle Detection Systems. , 2007, , .		3

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163	A Versatile Setup to Test and Program an Incremental Analog-to-Digital Converter. , 2007, , .		0
164	Versatile Automotive Sensor Interface ASIC. , 2007, , .		3
165	A pm0.75-V Compact CMOS Class-AB Current-Mode Exponential Variable Gain Amplifier. IEEE Transactions on Circuits and Systems II: Express Briefs, 2007, 54, 1042-1046.	3.0	13
166	Class AB Pseudo-Differential CMOS Squarer Circuit. , 2007, , .		3
167	Low-voltage, low-power rail-to-rail two stage op-amp with dynamic biasing and no Miller compensation. Midwest Symposium on Circuits and Systems, 2007, , .	1.0	3
168	Single Transistor High-Impedance Tail Current Source With Extended Common-Mode Input Range and Reduced Supply Requirements. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2007, 54, 581-585.	2.2	3
169	A High-Swing, High-Speed CMOS WTA Using Differential Flipped Voltage Followers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2007, 54, 668-672.	3.0	21
170	Low-voltage FGMOS-based balanced current scaling in moderate inversion. , 2007, , .		1
171	Alternative algorithm for low voltage operation of incremental ADCs. , 2007, , .		0
172	Â±1.5â€¦V 3â€¦mW CMOS Vâ€“I converter with 75â€¦dB SFDR for 6â€¦Vpp input swings. Electronics Letters, 2007, 43, 336.	1.0	4
173	Versatile multidecade CMOS voltage controlled oscillator with accurate amplitude and PWM control. Midwest Symposium on Circuits and Systems, 2007, , .	1.0	1
174	Multiple operating points in a square-root domain first-order filter. International Journal of Circuit Theory and Applications, 2007, 35, 71-91.	2.0	9
175	Highly linear wide input range CMOS OTA architectures operating in subthreshold and strong inversion. Microelectronic Engineering, 2007, 84, 273-279.	2.4	5
176	Low-Voltage CMOS Single Ended and Fully Differential Amplifier with Programmable Gain.. , 2007, , .		0
177	Low voltage gain boosting schemes for one stage operational amplifiers. , 2007, , .		1
178	Super Class-AB OTAs With Adaptive Biasing and Dynamic Output Current Scaling. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 449-457.	0.1	90
179	A low-voltage low-power QFG-based Sigma-Delta modulator for electroencephalogram applications. , 2006, , .		8
180	New Gain Programmable Current Mirrors Based on Current Steering. Midwest Symposium on Circuits and Systems, 2006, , .	1.0	5

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181	Low-Voltage Universal Cell (LVUC): A Compact Analog/Digital Logic Block for Mixed Signal FPGAs. , 2006, , .		1
182	Highly linear programmable balanced current scaling technique in moderate inversion. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 283-285.	2.2	28
183	New low-voltage class AB/AB CMOS op amp with rail-to-rail input/output swing. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 289-293.	2.2	13
184	Simple class-AB voltage follower with slew rate and bandwidth enhancement and no extra static power or supply requirements. Electronics Letters, 2006, 42, 784.	1.0	50
185	A free but efficient low-voltage class-AB two-stage operational amplifier. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 568-571.	2.2	126
186	Novel Low-Power High-dB Range CMOS Pseudo-Exponential Cells. ETRI Journal, 2006, 28, 732-738.	2.0	12
187	Winner-Take-All Class AB Input Stage. Analog Integrated Circuits and Signal Processing, 2006, 46, 149-152.	1.4	6
188	Compact Novel Floating Gate Offset Compensation Scheme with Low Sensitivity to Charge Injection, Clock Feedthrough and Leakage. , 2006, , .		0
189	Compact multiple output linear V to I converters using split differential pairs and applications in allpass OTAs. Electronics Letters, 2006, 42, 783.	1.0	0
190	Gain programmable current mirrors based on current steering. Electronics Letters, 2006, 42, 559.	1.0	17
191	Compact power-efficient class-AB CMOS exponential voltage to voltage converter. Electronics Letters, 2006, 42, 127.	1.0	5
192	Input offset compensation scheme with reduced sensitivity to charge injection and leakage. Electronics Letters, 2006, 42, 340.	1.0	2
193	A Very Low-Power Class AB/AB Op-amp based Sigma-Delta Modulator for Biomedical Applications. Midwest Symposium on Circuits and Systems, 2006, , .	1.0	1
194	Two-Stage Differential Charge and Transresistance Amplifiers. Conference Record - IEEE Instrumentation and Measurement Technology Conference, 2006, , .	0.0	4
195	Super Class AB OTAs Based on Low-Power Adaptive Techniques at the Input Stage and the Active Load. Journal of Low Power Electronics, 2006, 2, 317-324.	0.6	2
196	Low-voltage wide gm adjustable range highly linear BiCMOS OTA. IEICE Electronics Express, 2005, 2, 127-132.	0.8	2
197	Rail-to-rail super class AB CMOS operational amplifiers. Electronics Letters, 2005, 41, 1.	1.0	23
198	A proposal for high-performance CCII-based analogue CMOS design. International Journal of Circuit Theory and Applications, 2005, 33, 379-391.	2.0	19

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199	Novel Architectures of Class AB CMOS Mirrors with Programmable Gain. Analog Integrated Circuits and Signal Processing, 2005, 42, 197-202.	1.4	9
200	Compact implementation of high-performance CMOS current mirror. Electronics Letters, 2005, 41, 570.	1.0	18
201	Linearisation of MOS resistors using capacitive gate voltage averaging. Electronics Letters, 2005, 41, 511.	1.0	22
202	1.5-V current-mode CMOS true RMS-DC converter based on class-AB transconductors. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2005, 52, 376-379.	2.2	26
203	Low-Voltage Super class AB CMOS OTA cells with very high slew rate and power efficiency. IEEE Journal of Solid-State Circuits, 2005, 40, 1068-1077.	5.4	239
204	A CMOS transconductor with multidecade tuning using balanced current scaling in moderate inversion. IEEE Journal of Solid-State Circuits, 2005, 40, 1078-1083.	5.4	47
205	High-speed high-precision CMOS analog rank order filter with O(n) complexity. IEEE Journal of Solid-State Circuits, 2005, 40, 1238-1248.	5.4	7
206	The flipped voltage follower: a useful cell for low-voltage low-power circuit design. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2005, 52, 1276-1291.	0.1	532
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