Shuyu Bao

List of Publications by Year in descending order

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687220 794469 29 658 13 19 h-index citations g-index papers 29 29 29 800 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	A review of silicon-based wafer bonding processes, an approach to realize the monolithic integration of Si-CMOS and Ill–V-on-Si wafers. Journal of Semiconductors, 2021, 42, 023106.	2.0	34
2	Systematic Investigation and Characterization of Ag Paste for LED Die Attach., 2021, , .		4
3	Ge-on-insulator lateral p-i-n waveguide photodetectors for optical communication. Optics Letters, 2020, 45, 6683.	1.7	7
4	Strain relaxation of germanium-tin (GeSn) fins. AIP Advances, 2018, 8, 025111.	0.6	6
5	Suppression of interfacial voids formation during silane (SiH4)-based silicon oxide bonding with a thin silicon nitride capping layer. Journal of Applied Physics, 2018, 123, .	1.1	14
6	Monolithic Integration of Si-CMOS and III-V-on-Si Through Direct Wafer Bonding Process. IEEE Journal of the Electron Devices Society, 2018, 6, 571-578.	1.2	19
7	High-performance AlGaInP light-emitting diodes integrated on silicon through a superior quality germanium-on-insulator. Photonics Research, 2018, 6, 290.	3.4	8
8	Hetero-epitaxy of high quality germanium film on silicon substrate for optoelectronic integrated circuit applications. Journal of Materials Research, 2017, 32, 4025-4040.	1.2	15
9	Germanium-on-insulator virtual substrate for InGaP epitaxy. Materials Science in Semiconductor Processing, 2017, 70, 17-23.	1.9	O
10	The first GeSn FinFET on a novel GeSnOI substrate achieving lowest S of 79 mV/decade and record high Gm, int of 807 \hat{l}_4 S/ \hat{l}_4 m for GeSn P-FETs. , 2017, , .		18
11	Germanium-on-insulator virtual substrate for InGaP epitaxy. Materials Science in Semiconductor Processing, 2017, 58, 15-21.	1.9	5
12	Low-threshold optically pumped lasing in highly strained germanium nanowires. Nature Communications, 2017, 8, 1845.	5.8	131
13	Thermal stability of germanium-tin (GeSn) fins. Applied Physics Letters, 2017, 111, 252103.	1.5	7
14	Extension of Germanium-on-insulator optical absorption edge using CMOS-compatible silicon nitride stressor., 2017,,.		4
15	High-efficiency normal-incidence vertical p-i-n photodetectors on a germanium-on-insulator platform. Photonics Research, 2017, 5, 702.	3.4	52
16	Modeling and fabrication of Ge-on-Si <inf>3</inf> N <inf>4</inf> for low bend-loss waveguides., 2016,,.		0
17	Reduction of threading dislocation density in Ge/Si using a heavily As-doped Ge seed layer. AIP Advances, 2016, 6, .	0.6	47
18	GeSn-on-insulator substrate formed by direct wafer bonding. Applied Physics Letters, 2016, 109, .	1.5	31

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#	Article	IF	CITATIONS
19	Epitaxy and wafer bonding of AlGalnP multiple-quantum wells and light-emitting diodes on $8\hat{a} \in \mathbb{R}^3$ Si substrates. , 2016, , .		0
20	Germanium-on-silicon nitride waveguides for mid-infrared integrated photonics. Applied Physics Letters, $2016,109,.$	1.5	66
21	High quality Ge-OI, III–V-OI on 200 mm Si substrate. , 2016, , .		0
22	Integration of GaAs, GaN, and Si-CMOS on a common 200 mm Si substrate through multilayer transfer process. Applied Physics Express, 2016, 9, 086501.	1.1	33
23	Defects reduction of Ge epitaxial film in a germanium-on-insulator wafer by annealing in oxygen ambient. APL Materials, 2015, 3, .	2.2	43
24	Integration of Ill–V materials and Si-CMOS through double layer transfer process. Japanese Journal of Applied Physics, 2015, 54, 030209.	0.8	21
25	The role of AsH3 partial pressure on anti-phase boundary in GaAs-on-Ge grown by MOCVD – Application to a 200mm GaAs virtual substrate. Journal of Crystal Growth, 2015, 421, 58-65.	0.7	28
26	Monolithic integration of III–V HEMT and Si-CMOS through TSV-less 3D wafer stacking. , 2015, , .		14
27	Fabrication and characterization of germanium-on-insulator through epitaxy, bonding, and layer transfer. Journal of Applied Physics, 2014, 116, .	1.1	49
28	Fabrication of germanium-on-insulator (GOI) with improved threading dislocation density (TDD) via buffer-less epitaxy and bonding. , 2014, , .		2
29	Integration of III–V materials and Si-CMOS through double layer transfer process. , 2014, , .		O