

Chao Du

List of Publications by Year in descending order

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Version: 2024-02-01

15
papers

2,568
citations

759233

12
h-index

1125743

13
g-index

15
all docs

15
docs citations

15
times ranked

2698
citing authors

#	ARTICLE	IF	CITATIONS
1	Reservoir computing using dynamic memristors for temporal information processing. Nature Communications, 2017, 8, 2204.	12.8	547
2	Sparse coding with memristor networks. Nature Nanotechnology, 2017, 12, 784-789.	31.5	510
3	Experimental Demonstration of a Second-Order Memristor and Its Ability to Biorealistically Implement Synaptic Plasticity. Nano Letters, 2015, 15, 2203-2211.	9.1	473
4	Biorealistic Implementation of Synaptic Functions with Oxide Memristors through Internal Ionic Dynamics. Advanced Functional Materials, 2015, 25, 4290-4299.	14.9	360
5	Temporal data classification and forecasting using a memristor-based reservoir computing system. Nature Electronics, 2019, 2, 480-487.	26.0	309
6	Tuning Ionic Transport in Memristive Devices by Graphene with Engineered Nanopores. ACS Nano, 2016, 10, 3571-3579.	14.6	139
7	Emulation of synaptic metaplasticity in memristors. Nanoscale, 2017, 9, 45-51.	5.6	73
8	Feature Extraction Using Memristor Networks. IEEE Transactions on Neural Networks and Learning Systems, 2016, 27, 2327-2336.	11.3	62
9	Field-Programmable Crossbar Array (FPCA) for Reconfigurable Computing. IEEE Transactions on Multi-Scale Computing Systems, 2018, 4, 698-710.	2.4	28
10	Interference and memory capacity effects in memristive systems. Applied Physics Letters, 2013, 102, .	3.3	17
11	FPAA/Memristor Hybrid Computing Infrastructure. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 906-915.	5.4	17
12	Temporal information encoding in dynamic memristive devices. Applied Physics Letters, 2015, 107, .	3.3	13
13	Device nonideality effects on image reconstruction using memristor arrays. , 2016, , .		12
14	3-D Vertical Dual-Layer Oxide Memristive Devices. IEEE Transactions on Electron Devices, 2014, 61, 2581-2583.	3.0	6
15	Analog signal processing on a FPAA/memristor hybrid circuit. , 2014, , .		2