

Weixing Ji

List of Publications by Year in descending order

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Version: 2024-02-01

11
papers

100
citations

1684188

5
h-index

1720034

7
g-index

11
all docs

11
docs citations

11
times ranked

65
citing authors

| # | ARTICLE | IF | CITATIONS |
|----|---|-----|-----------|
| 1 | An adaptive and hierarchical task scheduling scheme for multi-core clusters. <i>Parallel Computing</i> , 2014, 40, 611-627. | 2.1 | 9 |
| 2 | A Hierarchical Work-Stealing Framework for Multi-core Clusters. , 2012, , . | | 2 |
| 3 | Knowledge-Based Adaptive Self-Scheduling. <i>Lecture Notes in Computer Science</i> , 2012, , 22-32. | 1.3 | 7 |
| 4 | Core Working Set Based Scratchpad Memory Management. <i>IEICE Transactions on Information and Systems</i> , 2011, E94-D, 274-285. | 0.7 | 3 |
| 5 | 3D floorplanning of low-power and area-efficient Network-on-Chip architecture. <i>Microprocessors and Microsystems</i> , 2011, 35, 484-495. | 2.8 | 10 |
| 6 | Dynamic and adaptive SPM management for a multi-task environment. <i>Journal of Systems Architecture</i> , 2011, 57, 181-192. | 4.3 | 10 |
| 7 | A Semi-automatic Scratchpad Memory Management Framework for CMP. <i>Lecture Notes in Computer Science</i> , 2011, , 73-87. | 1.3 | 3 |
| 8 | Computationally efficient locality-aware interconnection topology for multi-processor system-on-chip (MP-SoC). <i>Science Bulletin</i> , 2010, 55, 3363-3371. | 1.7 | 9 |
| 9 | A Novel Adaptive Scratchpad Memory Management Strategy. , 2009, , . | | 8 |
| 10 | A Triplet-based Computer Architecture Supporting Parallel Object Computing. , 2007, , . | | 26 |
| 11 | A New Hierarchical Interconnection Network for Multi-core Processor. , 2007, , . | | 13 |