Chun-Hsien Lien

List of Publications by Year
in descending order

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15	246	1040056	1474206
papers	citations	9	9
		h-index	g-index

15	15	15	178
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	A 1-17-GHz InGaP-GaAs HBT MMIC analog multiplier and mixer with broad-band input-matching networks. IEEE Transactions on Microwave Theory and Techniques, 2002, 50, 2564-2568.	4.6	37
2	THE YUAN-TSEH LEE ARRAY FOR MICROWAVE BACKGROUND ANISOTROPY. Astrophysical Journal, 2009, 694, 1610-1618.	4.5	35
3	A 78-114 GHz monolithic subharmonically pumped GaAs-based HEMT diode mixer. IEEE Microwave and Wireless Components Letters, 2002, 12, 209-211.	3.2	31
4	Analysis and Design of Reduced-Size Marchand Rat-Race Hybrid for Millimeter-Wave Compact Balanced Mixers in 130-nm CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 1966-1977.	4.6	24
5	Design and Analysis of Down-Conversion Gate/Base-Pumped Harmonic Mixers Using Novel Reduced-Size 180^circ Hybrid With Different Input Frequencies. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 2473-2485.	4.6	23
6	A wideband analog correlator system for AMiBA. , 2004, 5498, 455.		17
7	A Novel Reduced-Size Rat-Race Broadside Coupler and Its Application for CMOS Distributed Sub-Harmonic Mixer. IEEE Microwave and Wireless Components Letters, 2008, 18, 194-196.	3.2	17
8	AMiBA WIDEBAND ANALOG CORRELATOR. Astrophysical Journal, 2010, 716, 746-757.	4.5	17
9	60 GHz Double-Balanced Gate-Pumped Down-Conversion Mixers With a Combined Hybrid on 130 nm CMOS Processes. IEEE Microwave and Wireless Components Letters, 2010, 20, 160-162.	3.2	13
10	Broad-band MMICs based on modified loss-compensation method using 0.35-/spl mu/m SiGe BiCMOS technology. IEEE Transactions on Microwave Theory and Techniques, 2005, 53, 496-505.	4.6	10
11	A 0.1-23-GHz SiGe BiCMOS analog multiplier and mixer based on attenuation-compensation technique. , 0, , .		9
12	Ka-band monolithic GaAs PHEMT circuits for transceiver applications. , 0, , .		7
13	A 60-GHz single-balance gate-pumped down-conversion mixer with reduced-size rat-race hybrid on 130-nm CMOS process. , 2008, , .		6
14	A high-efficiency, broadband and high output power PHEMT balanced K-band doubler with integrated balun. , 2006, , .		0
15	A 1.2 V 15â€“32ÂGHz low-power single-balanced gate mixer with a miniature rat-race hybrid. International Journal of Microwave and Wireless Technologies, 2012, 4, 455-461.	1.9	0