Takashi Nakada

List of Publications by Year
in descending order

11 papers	**43** citations	2682572 **2** h-index	2550090 **3** g-index
11 all docs	**11** docs citations	**11** times ranked	**31** citing authors

#	Article	IF	Citations
1	Design of Programmable Analog Calculation Unit by Implementing Support Vector Regression for Approximate Computing. IEEE Micro, 2018, 38, 73-82.	1.8	13
2	An Instruction Mapping Scheme for FU Array Accelerator. IEICE Transactions on Information and Systems, 2011, E94-D, 286-297.	0.7	12
3	A Minimal Roll-Back Based Recovery Scheme for Fault Toleration in Pipeline Processors. , 2010, , .		4
4	Compression and Aggregation for Optimizing Information Transmission in Distributed CNN. , 2017, , .		4
5	DiaNet: An Efficient Multi-Grained Re-configurable Neural Network in Silicon. , 2019, , .		4
6	An efficient and reliable 1.5-way processor by fusion of space and time redundancies. , 2011, , .		3
7	A Feasibility Study of Programmable Analog Calculation Unit for Approximate Computing. , 2017, , .		2
8	RazorProtector: Maintaining Razor DVS Efficiency in Large IR-Drop Zones by an Adaptive Redundant Data-Path. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2012, E95.A, 2319-2329.	0.3	1
9	[2009] An Instruction Decomposition Method for Reconfigurable Decoders. , 2010, , .		0
10	CPRring: A Structure-Aware Ring-Based Checkpointing Architecture for FPGA Computing. , 2017, , .		0
11	A programmable analog calculation unit for vector computations. , 2018, , .		0