Kimmo Järvinen

List of Publications by Year in descending order

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1478505 1872680 9 255 6 6 citations h-index g-index papers 9 9 9 206 docs citations times ranked citing authors all docs

#	Article	IF	Citations
1	On Parallelization of High-Speed Processors for Elliptic Curve Cryptography. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2008, 16, 1162-1175.	3.1	74
2	Elliptic Curve Cryptography with Efficiently Computable Endomorphisms and Its Hardware Implementations for the Internet of Things. IEEE Transactions on Computers, 2017, 66, 773-785.	3.4	49
3	Fast point multiplication on Koblitz curves: Parallelization method and implementations. Microprocessors and Microsystems, 2009, 33, 106-116.	2.8	35
4	Optimized FPGA-based elliptic curve cryptography processor for high-speed applications. The Integration VLSI Journal, 2011, 44, 270-279.	2.1	35
5	Fast Inversion in <formula formulatype="inline"><tex Notation="TeX">\${schmi{GF(2^m)}}\$</tex </formula> with Normal Basis Using Hybrid-Double Multipliers. IEEE Transactions on Computers, 2014, 63, 1041-1047.	3.4	19
6	Efficient Circuitry for Computing τ-adic Non-Adjacent Form. , 2006, , .		14
7	Harnessing Biased Faults in Attacks on ECC-Based Signature Schemes. , 2012, , .		10
8	A Generalization of Addition Chains and Fast Inversions in Binary Fields. IEEE Transactions on Computers, 2015, 64, 2421-2432.	3.4	10
9	Another Look at Inversions over Binary Fields. , 2013, , .		9