

# Christopher M Twigg

## List of Publications by Year in descending order

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Version: 2024-02-01

22  
papers

502  
citations

1307594

7  
h-index

1474206

9  
g-index

22  
all docs

22  
docs citations

22  
times ranked

414  
citing authors

#	ARTICLE	IF	CITATIONS
1	Synthesis of a reconfigurable analog vector-sum circuit into the MTL-Based configurable blocks. <i>Microelectronics Journal</i> , 2018, 75, 97-104.	2.0	2
2	Rank Determination by Winner-Take-All Circuit for Rank Modulation Memory. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2016, 63, 326-330.	3.0	6
3	A Ramp Converter Approach to Rank Modulation. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2015, 62, 866-870.	3.0	0
4	MTL-based implementation of current-mode CMOS RMS-to-DC converters. <i>International Journal of Circuit Theory and Applications</i> , 2015, 43, 793-805.	2.0	18
5	A Fully Reconfigurable Low-Noise Biopotential Sensing Amplifier With 1.96 Noise Efficiency Factor. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2014, 8, 411-422.	4.0	135
6	An MTL-Based Configurable Block for Current-Mode Nonlinear Analog Computation. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2013, 60, 587-591.	3.0	19
7	Rank determination algorithm by current comparing for rank modulation flash memories. , 2013, , .		9
8	A self-powered adaptive wireless sensor network for wastewater treatment plants. , 2011, , .		5
9	A Floating-Gate-Based Field-Programmable Analog Array. <i>IEEE Journal of Solid-State Circuits</i> , 2010, 45, 1781-1794.	5.4	109
10	A large-scale Reconfigurable Smart Sensory Chip. , 2009, , .		15
11	Incorporating Large-Scale FPAA's Into Analog Design and Test Courses. <i>IEEE Transactions on Education</i> , 2008, 51, 319-324.	2.4	18
12	A Floating-Gate-Based Programmable CMOS Reference. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2008, 55, 3448-3456.	5.4	43
13	RASP 2.8: A new generation of floating-gate based field programmable analog array. , 2008, , .		23
14	A Self-Contained Large-Scale FPAA Development Platform. , 2007, , .		2
15	Programmable Conductance Switches for FPAA's. , 2007, , .		5
16	Large-Scale FPAA Devices for Signal Processing Applications. , 2007, , .		2
17	An OTA-based Large-Scale Field Programmable Analog Array (FPAA) for faster On-Chip Communication and Computation. , 2007, , .		13
18	Incorporating Large-Scale FPAA's in Analog Design Courses. , 2007, , .		2

#	ARTICLE	IF	CITATIONS
19	Programmable Floating Gate FPAA Switches Are Not Dead Weight. , 2007, , .		30
20	A Large-Scale Reconfigurable Analog Signal Processor (RASP) IC. , 2006, , .		29
21	A Compact Programmable CMOS Reference With $\hat{\pm}40^{1/4}V$ Accuracy. , 2006, , .		6
22	Developing large-scale field-programmable analog arrays for rapid prototyping. International Journal of Embedded Systems, 2005, 1, 179.	0.3	11