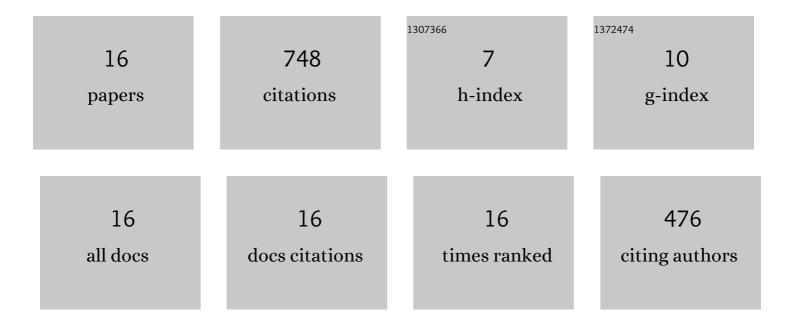
## Mary Sheeran

List of Publications by Year in descending order

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Μλον Sheedan

#	Article	IF	CITATIONS
1	A language for hierarchical data parallel design-space exploration on GPUs. Journal of Functional Programming, 2016, 26, .	0.5	0
2	Functional programming and hardware design: still interesting after all these years. , 2015, , .		0
3	Functional programming and hardware design: still interesting after all these years. ACM SIGPLAN Notices, 2015, 50, 165-165.	0.2	0
4	Design exploration through code-generating DSLs. Communications of the ACM, 2014, 57, 56-63.	3.3	115
5	Expressive array constructs in an embedded GPU kernel programming language. , 2012, , .		58
6	Functional and dynamic programming in the design of parallel prefix networks. Journal of Functional Programming, 2011, 21, 59-114.	0.5	10
7	GPGPU kernel implementation and refinement using Obsidian. Procedia Computer Science, 2010, 1, 2065-2074.	1.2	20
8	Feldspar: A domain specific language for digital signal processing algorithms. , 2010, , .		60
9	SAT-Solving in Practice, with a Tutorial Example from Supervisory Control. Discrete Event Dynamic Systems: Theory and Applications, 2009, 19, 495-524.	0.6	26
10	SAT-solving in practice. , 2008, , .		12
11	Generating Fast Multipliers Using Clever Circuits. Lecture Notes in Computer Science, 2004, , 6-20.	1.0	7
12	Using Lava to design and verify recursive and periodic sorters. International Journal on Software Tools for Technology Transfer, 2003, 4, 349-358.	1.7	8
13	Finding Regularity: Describing and Analysing Circuits That Are Not Quite Regular. Lecture Notes in Computer Science, 2003, , 4-18.	1.0	5
14	The Design and Verification of a Sorter Core. Lecture Notes in Computer Science, 2001, , 355-368.	1.0	12
15	A Tutorial on Stålmarck's Proof Procedure for Propositional Logic. Formal Methods in System Design, 2000, 16, 23-58.	0.9	72
16	Checking Safety Properties Using Induction and a SAT-Solver. Lecture Notes in Computer Science, 2000, , 127-144.	1.0	343