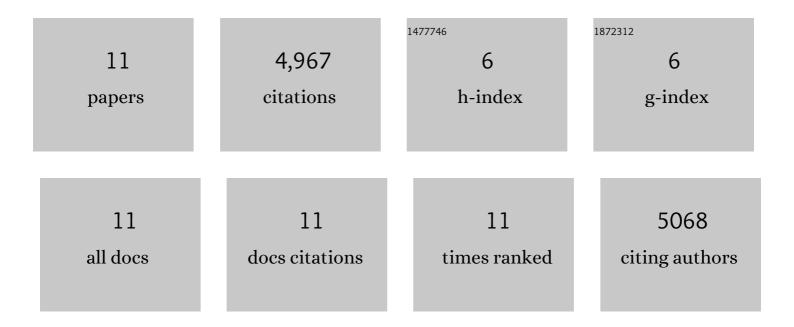
## Sung Hyun Jo

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Nanoscale Memristor Device as Synapse in Neuromorphic Systems. Nano Letters, 2010, 10, 1297-1301.	4.5	3,507
2	High-Density Crossbar Arrays Based on a Si Memristive System. Nano Letters, 2009, 9, 870-874.	4.5	507
3	CMOS Compatible Nanoscale Nonvolatile Resistance Switching Memory. Nano Letters, 2008, 8, 392-397.	4.5	411
4	Programmable Resistance Switching in Nanoscale Two-Terminal Devices. Nano Letters, 2009, 9, 496-500.	4.5	272
5	Nanoscale resistive memory with intrinsic diode characteristics and long endurance. Applied Physics Letters, 2010, 96, .	1.5	144
6	Cross-Point Resistive RAM Based on Field-Assisted Superlinear Threshold Selector. IEEE Transactions on Electron Devices, 2015, 62, 3477-3481.	1.6	90
7	Si Memristive devices applied to memory and neuromorphic circuits. , 2010, , .		24
8	Self-limited RRAM with ON/OFF resistance ratio amplification. , 2015, , .		10
9	Ag/a-Si:H/c-Si resistive switching nonvolatile memory devices. , 2006, , .		1
10	Resistive random access memory with high selectivity and ON/OFF ratio amplification sensing. , 2015, , .		1
11	Si-based two-terminal resistive switching nonvolatile memory. , 2008, , .		Ο