

Rakesh Kumar Palani

List of Publications by Year in descending order

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Version: 2024-02-01

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3311381

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3475538

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#	ARTICLE	IF	CITATIONS
1	ADC. Analog Circuits and Signal Processing Series, 2017, , 103-121.	0.3	0
2	A 220-MS/s 9-Bit 2X Time-Interleaved SAR ADC With a 133-fF Input Capacitance and a FOM of 37 fj/conv in 65-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 1053-1057.	3.0	7