

Vobulapuram Ramesh Kumar

List of Publications by Year in descending order

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15
papers

375
citations

1040056

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1199594

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16
times ranked

167
citing authors

#	ARTICLE	IF	CITATIONS
1	A prominent unified crosstalk model for linear and sub-threshold regions in mixed CNT bundle interconnects. <i>Microelectronics Journal</i> , 2021, 118, 105294.	2.0	4
2	Design of MWCNT based Through Silicon Vias with Polymer Liners to Reduce the Crosstalk Effects. <i>ECS Journal of Solid State Science and Technology</i> , 2020, 9, 041002.	1.8	9
3	Effect of Skin Impedance on Delay and Crosstalk in Lossy and Non-uniform On-Chip Interconnects. <i>Advances in Intelligent Systems and Computing</i> , 2018, , 569-576.	0.6	1
4	Improved crosstalk noise modeling of MWCNT interconnects using FDTD technique. <i>Microelectronics Journal</i> , 2015, 46, 1263-1268.	2.0	22
5	Accurate Numerical Model for Crosstalk Analysis of SWCNT Bundle Interconnects Using FDTD Method. , 2015, , .		2
6	Crosstalk noise modeling of multiwall carbon nanotube (MWCNT) interconnects using finite-difference time-domain (FDTD) technique. <i>Microelectronics Reliability</i> , 2015, 55, 155-163.	1.7	32
7	Stability and delay analysis of multi-layered GNR and multi-walled CNT interconnects. <i>Journal of Computational Electronics</i> , 2015, 14, 611-618.	2.5	43
8	Time and Frequency Domain Analysis of MLG NR Interconnects. <i>IEEE Nanotechnology Magazine</i> , 2015, 14, 484-492.	2.0	65
9	Performance analysis of single- and multi-walled carbon nanotube based through silicon vias. , 2015, , .		12
10	Crosstalk modeling with width dependent MFP in MLG NR interconnects using FDTD technique. , 2015, , .		2
11	Crosstalk Induced Delay Analysis of Randomly Distributed Mixed CNT Bundle Interconnect. <i>Journal of Circuits, Systems and Computers</i> , 2015, 24, 1550145.	1.5	22
12	Graphene Based On-Chip Interconnects and TSVs : Prospects and Challenges. <i>IEEE Nanotechnology Magazine</i> , 2014, 8, 14-20.	1.3	36
13	Carbon Nanotube Based 3-D Interconnects - A Reality or a Distant Dream. <i>IEEE Circuits and Systems Magazine</i> , 2014, 14, 16-35.	2.3	34
14	An accurate model for dynamic crosstalk analysis of CMOS gate driven on-chip interconnects using FDTD method. <i>Microelectronics Journal</i> , 2014, 45, 441-448.	2.0	42
15	An Accurate FDTD Model for Crosstalk Analysis of CMOS-Gate-Driven Coupled RLC Interconnects. <i>IEEE Transactions on Electromagnetic Compatibility</i> , 2014, 56, 1185-1193.	2.2	48