

# Debesh Bhatta

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/12063885/publications.pdf>

Version: 2024-02-01

14  
papers

49  
citations

2682572

2  
h-index

2917675

2  
g-index

14  
all docs

14  
docs citations

14  
times ranked

40  
citing authors

#	ARTICLE	IF	CITATIONS
1	Low Cost Sparse Multiband Signal Characterization Using Asynchronous Multi-Rate Sampling: Algorithms and Hardware. Journal of Electronic Testing: Theory and Applications (JETTA), 2015, 31, 85-98.	1.2	9
2	A Noise Aware CML Latch Modelling for Large System Simulation. , 2015, , .		0
3	Incoherent Undersampling-Based Waveform Reconstruction Using a Time-Domain Zero-Crossing Metric. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 2357-2370.	3.1	4
4	Timing Variation Adaptive Pipeline Design: Using Probabilistic Activity Completion Sensing with Backup Error Resilience. , 2014, , .		0
5	Low Cost Signal Reconstruction Based Testing of RF Components using Incoherent Undersampling. Journal of Electronic Testing: Theory and Applications (JETTA), 2014, 30, 213-228.	1.2	0
6	Periodic Jitter and Bounded Uncorrelated Jitter Decomposition Using Incoherent Undersampling. , 2013, , .		6
7	Low cost signal reconstruction based testing of RF components using incoherent undersampling. , 2013, , .		3
8	Time Domain Reconstruction of Incoherently Undersampled Periodic Waveforms Using Bandwidth Interleaving. , 2013, , .		1
9	Low-cost wideband periodic signal reconstruction using incoherent undersampling and back-end cost optimization. , 2012, , .		7
10	24GHZ dual core PLL design for 60 GHz transceiver and efficient validation methodology. , 2012, , .		0
11	Spectral Estimation Based Acquisition of Incoherently Under-sampled Periodic Signals: Application to Bandwidth Interleaving. , 2012, , .		2
12	An adaptive broadband BiCMOS active spur canceller. , 2011, , .		2
13	Time Domain Characterization and Test of High Speed Signals Using Incoherent Sub-sampling. , 2011, , .		8
14	A 10Gb/s two dimensional scanning eye opening monitor in 0.18um CMOS process. , 2009, , .		7