

# Steven Derrien

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/12033774/publications.pdf>

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11  
papers

120  
citations

1684188

5  
h-index

1474206

9  
g-index

11  
all docs

11  
docs citations

11  
times ranked

124  
citing authors

#	ARTICLE	IF	CITATIONS
1	On Model Subtyping. Lecture Notes in Computer Science, 2012, , 400-415.	1.3	42
2	Parallelizing HMMER for Hardware Acceleration on FPGAs. , 2007, , .		20
3	Compiling Scilab to high performance embedded multicore systems. Microprocessors and Microsystems, 2013, 37, 1033-1049.	2.8	12
4	Cluster of re-configurable nodes for scanning large genomic banks. Parallel Computing, 2005, 31, 73-96.	2.1	10
5	Hardware Acceleration of HMMER on FPGAs. Journal of Signal Processing Systems, 2010, 58, 53-67.	2.1	10
6	Combining Flash Memory and FPGAs to Efficiently Implement a Massively Parallel Algorithm for Content-Based Image Retrieval. , 2007, , 247-258.		7
7	Bridging the chasm between MDE and the world of compilation. Software and Systems Modeling, 2012, 11, 581-597.	2.7	5
8	Worst-Case Execution-Time-Aware Parallelization of Model-Based Avionics Applications. Journal of Aerospace Information Systems, 2019, 16, 521-533.	1.4	4
9	Model-Driven Engineering and Optimizing Compilers: A Bridge Too Far?. Lecture Notes in Computer Science, 2011, , 608-622.	1.3	4
10	Combining flash memory and FPGAs to efficiently implement a massively parallel algorithm for content-based image retrieval. International Journal of Electronics, 2008, 95, 621-635.	1.4	3
11	Combining execution pipelines to improve parallel implementation of HMMER on FPGA. Microprocessors and Microsystems, 2015, 39, 457-470.	2.8	3