

Swapnadip De

List of Publications by Year in descending order

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6
papers

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citations

2258059

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2053705

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g-index

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6
docs citations

6
times ranked

49
citing authors

#	ARTICLE	IF	CITATIONS
1	Parameter modeling of linearly doped double gate MOSFET with high-k dielectrics. , 2017, , .		3
2	Implementation of Subthreshold Adiabatic Logic for Ultralow-Power Application. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 2782-2790.	3.1	43
3	Modeling of characteristic parameters for nano-scale junctionless double gate MOSFET considering quantum mechanical effect. Journal of Computational Electronics, 2015, 14, 262-269.	2.5	18
4	Modeling of parameters for nano-scale surrounding-gate MOSFET considering quantum mechanical effect. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2014, 27, 883-895.	1.9	2
5	Asymmetric halo and symmetric Single-Halo Dual-Material Gate and Double-Halo Dual-Material Gate n-MOSFETs characteristic parameter modeling. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2013, 26, 41-55.	1.9	14
6	Modelling of parameters for asymmetric halo and symmetric DHDMG n-MOSFETs. International Journal of Electronics, 2011, 98, 1365-1381.	1.4	3