

# Swapnadip De

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/12003872/publications.pdf>

Version: 2024-02-01

6  
papers

83  
citations

2258059

3  
h-index

2053705

5  
g-index

6  
all docs

6  
docs citations

6  
times ranked

49  
citing authors

#	ARTICLE	IF	CITATIONS
1	Implementation of Subthreshold Adiabatic Logic for Ultralow-Power Application. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 2782-2790.	3.1	43
2	Modeling of characteristic parameters for nano-scale junctionless double gate MOSFET considering quantum mechanical effect. Journal of Computational Electronics, 2015, 14, 262-269.	2.5	18
3	Asymmetric halo and symmetric Singleâ€Halo Dualâ€Material Gate and Doubleâ€Halo Dualâ€Material Gate nâ€MOSFETs characteristic parameter modeling. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2013, 26, 41-55.	1.9	14
4	Modelling of parameters for asymmetric halo and symmetric DHDMG n-MOSFETs. International Journal of Electronics, 2011, 98, 1365-1381.	1.4	3
5	Parameter modeling of linearly doped double gate MOSFET with high-k dielectrics. , 2017, , .		3
6	Modeling of parameters for nanoâ€scale surroundingâ€gate MOSFET considering quantum mechanical effect. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2014, 27, 883-895.	1.9	2