

Nicolas Vasilache

List of Publications by Year in descending order

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8
papers

575
citations

1937685

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2053705

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g-index

9
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9
docs citations

9
times ranked

229
citing authors

#	ARTICLE	IF	CITATIONS
1	MLIR: Scaling Compiler Infrastructure for Domain Specific Computation. , 2021, , .		132
2	The Next 700 Accelerated Layers. Transactions on Architecture and Code Optimization, 2019, 16, 1-26.	2.0	27
3	Tiling and optimizing time-iterated computations on periodic domains. , 2014, , .		25
4	Loop transformations. ACM SIGPLAN Notices, 2011, 46, 549-562.	0.2	32
5	Automatic Correction of Loop Transformations. Parallel Architecture and Compilation Techniques (PACT), Proceedings of the International Conference on, 2007, , .	0.0	4
6	Iterative Optimization in the Polyhedral Model: Part I, One-Dimensional Time. , 2007, , .		77
7	Semi-Automatic Composition of Loop Transformations for Deep Parallelism and Memory Hierarchies. International Journal of Parallel Programming, 2006, 34, 261-317.	1.5	156
8	Facilitating the search for compositions of program transformations. , 2005, , .		72