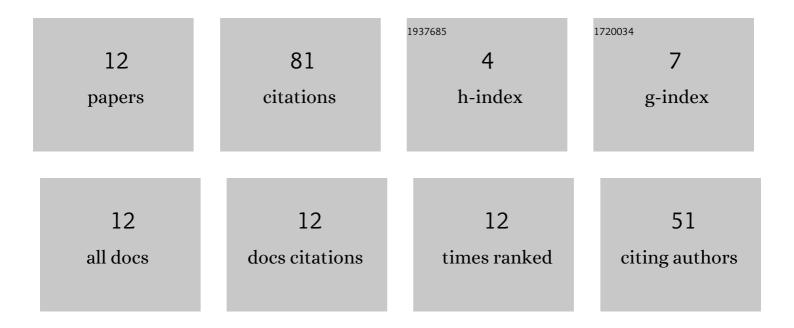
John Paul Shen

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Value locality and load value prediction. ACM SIGPLAN Notices, 1996, 31, 138-147.	0.2	33
2	Instruction path coprocessors. , 0, , .		14
3	Value locality and load value prediction. Operating Systems Review (ACM), 1996, 30, 138-147.	1.9	10
4	A Buffer-Oriented Methodology for Microarchitecture Validation. Journal of Electronic Testing: Theory and Applications (JETTA), 2000, 16, 49-65.	1.2	9
5	The block-based trace cache. Computer Architecture News, 1999, 27, 196-207.	2.5	8
6	Completion time multiple branch prediction for enhancing trace cache performance. Computer Architecture News, 2000, 28, 47-58.	2.5	3
7	Instruction path coprocessors. Computer Architecture News, 2000, 28, 270-281.	2.5	3
8	Post-pass partitioning of signal processing programs. International Journal of Parallel Programming, 1997, 25, 245-280.	1.5	1
9	The intrinsic bandwidth requirements of ordinary programs. Operating Systems Review (ACM), 1996, 30, 105-114.	1.9	0
10	Compiler Support for Low-Cost Synchronization Among Threads. Advances in Parallel Computing, 1998, , 485-494.	0.3	0
11	Deep Speaker Embedding for Speaker-Targeted Automatic Speech Recognition. , 2019, , .		0
12	The intrinsic bandwidth requirements of ordinary programs. ACM SIGPLAN Notices, 1996, 31, 105-114.	0.2	0