

Yaoyu Tao

List of Publications by Year in descending order

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6
papers

52
citations

2258059

3
h-index

2550090

3
g-index

6
all docs

6
docs citations

6
times ranked

43
citing authors

#	ARTICLE	IF	CITATIONS
1	A Fully Parallel Nonbinary LDPC Decoder With Fine-Grained Dynamic Clock Gating. IEEE Journal of Solid-State Circuits, 2015, 50, 464-475.	5.4	26
2	A Configurable Successive-Cancellation List Polar Decoder Using Split-Tree Architecture. IEEE Journal of Solid-State Circuits, 2021, 56, 612-623.	5.4	14
3	High-throughput architecture and implementation of regular $(2, d \times c)$ nonbinary LDPC decoders. , 2012, , .		7
4	Efficient Post-Processors for Improving Error-Correcting Performance of LDPC Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 4032-4043.	5.4	3
5	A 3.25Gb/s, 13.2pJ/b, 0.64mm ² Configurable Successive-Cancellation List Polar Decoder using Split-Tree Architecture in 40nm CMOS. , 2019, , .		1
6	DNC-Aided SCL-Flip Decoding of Polar Codes. , 2021, , .		1