

Choongyeun Cho

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Wideband mmWave CML static divider in 65nm SOI CMOS technology. , 2008, , .		11
2	Scalable statistical measurement and estimation of a mmWave CML static divider sensitivity in 65nm SOI CMOS. , 2008, , .		2
3	A Low-Power mmWave CML Prescaler in 65nm SOI CMOS Technology. Compound Semiconductor Integrated Circuit Symposium (CSICS), IEEE, 2008, , .	0.0	3
4	Decomposition and Analysis of Process Variability Using Constrained Principal Component Analysis. IEEE Transactions on Semiconductor Manufacturing, 2008, 21, 55-62.	1.7	15
5	Manufacturable Parasitic-Aware Circuit-Level FETs in 65-nm SOI CMOS Technology. IEEE Electron Device Letters, 2007, 28, 520-522.	3.9	2
6	Symmetric Vertical Parallel Plate Capacitors for On-Chip RF Circuits in 65-nm SOI Technology. IEEE Electron Device Letters, 2007, 28, 616-618.	3.9	8