

# Siddharth Gaba

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11964213/publications.pdf>

Version: 2024-02-01

17  
papers

2,997  
citations

933447

10  
h-index

1199594

12  
g-index

17  
all docs

17  
docs citations

17  
times ranked

3588  
citing authors

#	ARTICLE	IF	CITATIONS
1	Observation of conducting filament growth in nanoscale resistive memories. Nature Communications, 2012, 3, 732.	12.8	957
2	A Functional Hybrid Memristor Crossbar-Array/CMOS System for Data Storage and Neuromorphic Applications. Nano Letters, 2012, 12, 389-395.	9.1	745
3	Stochastic memristive devices for computing and neuromorphic applications. Nanoscale, 2013, 5, 5872.	5.6	395
4	Synaptic behaviors and modeling of a metal oxide memristive device. Applied Physics A: Materials Science and Processing, 2011, 102, 857-863.	2.3	355
5	Nanoscale resistive memory with intrinsic diode characteristics and long endurance. Applied Physics Letters, 2010, 96, .	3.3	144
6	Very Low-Programming-Current RRAM With Self-Rectifying Characteristics. IEEE Electron Device Letters, 2016, 37, 404-407.	3.9	96
7	Device and SPICE modeling of RRAM devices. Nanoscale, 2011, 3, 3833.	5.6	84
8	Two-terminal resistive switches (memristors) for memory and logic applications. , 2011, , .		77
9	Conduction mechanism of a TaO <sub>x</sub> -based selector and its application in crossbar memory arrays. Nanoscale, 2015, 7, 4964-4970.	5.6	42
10	Memristive devices for stochastic computing. , 2014, , .		41
11	Ultralow Sub-1-nA Operating Current Resistive Memory With Intrinsic Non-Linear Characteristics. IEEE Electron Device Letters, 2014, 35, 1239-1241.	3.9	34
12	A Low-Power Variation-Aware Adaptive Write Scheme for Access-Transistor-Free Memristive Memory. ACM Journal on Emerging Technologies in Computing Systems, 2015, 12, 1-18.	2.3	10
13	3-D Vertical Dual-Layer Oxide Memristive Devices. IEEE Transactions on Electron Devices, 2014, 61, 2581-2583.	3.0	6
14	RRAM Solutions for Stochastic Computing. , 2019, , 153-164.		5
15	CMOS-integrated memristors for neuromorphic architectures. , 2011, , .		3
16	Improvement of RRAM Device Performance Through On-Chip Resistors. Materials Research Society Symposia Proceedings, 2012, 1430, 149.	0.1	2
17	Observation of conducting filament growth in nanoscale resistive memories. , 0, .		1