

Henry Selvaraj

List of Publications by Year in descending order

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29
papers

341
citations

1162367

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1125271

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29
all docs

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docs citations

29
times ranked

147
citing authors

#	ARTICLE	IF	CITATIONS
1	An application of functional decomposition in ROM-based FSM implementation in FPGA devices. Journal of Systems Architecture, 2005, 51, 424-434.	2.5	66
2	A General Approach to Boolean Function Decomposition and its Application in FPGABased Synthesis. VLSI Design, 1995, 3, 289-300.	0.5	49
3	A Survey of High Level Synthesis Languages, Tools, and Compilers for Reconfigurable High Performance Computing. Advances in Intelligent Systems and Computing, 2014, , 483-492.	0.5	32
4	Fast and efficient processor allocation algorithm for torus-based chip multiprocessors. Computers and Electrical Engineering, 2011, 37, 91-105.	3.0	19
5	Energy characteristic of a processor allocator and a network-on-chip. International Journal of Applied Mathematics and Computer Science, 2011, 21, 385-399.	1.5	18
6	Evaluation Scheme for NoC-based CMP with Integrated Processor Management System. International Journal of Electronics and Telecommunications, 2010, 56, 157-168.	0.5	17
7	Review of Packet Switching Technologies for Future NoC. , 2008, , .		16
8	Hardware implementation of processor allocation schemes for mesh-based chip multiprocessors. Microprocessors and Microsystems, 2010, 34, 39-48.	1.8	16
9	Accelerating High Performance Computing Applications: Using CPUs, GPUs, Hybrid CPU/GPU, and FPGAs. , 2012, , .		16
10	Processor Allocation Problem for NoC-Based Chip Multiprocessors. , 2009, , .		13
11	Efficient logic controller design. , 2010, , .		11
12	Synthesis of Processor Allocator for Torus-Based Chip MultiProcessors. , 2010, , .		10
13	Fast FPGA-based fault injection tool for embedded processors. , 2013, , .		10
14	MULTILEVEL SYNTHESIS OF FINITE STATE MACHINES BASED ON SYMBOLIC FUNCTIONAL DECOMPOSITION. International Journal of Computational Intelligence and Applications, 2006, 06, 257-271.	0.6	8
15	An efficient variable partitioning approach for functional decomposition of circuits. Journal of Systems Architecture, 2007, 53, 53-67.	2.5	8
16	FUNCTIONAL DECOMPOSITION â€” THE VALUE AND IMPLICATION FOR BOTH NEURAL NETWORKS AND DIGITAL DESIGNING. International Journal of Computational Intelligence and Applications, 2006, 06, 123-138.	0.6	7
17	Overlay-NoC and H-Phy based computing using modern Chip Multiprocessors. , 2012, , .		7
18	HYBRID APPROACH FOR BRAIN TUMOR SEGMENTATION IN MAGNETIC RESONANCE IMAGES USING CELLULAR NEURAL NETWORKS AND OPTIMIZATION TECHNIQUES. International Journal of Computational Intelligence and Applications, 2010, 09, 17-31.	0.6	6

#	ARTICLE	IF	CITATIONS
19	ITERATION-FREE FRACTAL CODING FOR IMAGE COMPRESSION USING GENETIC ALGORITHM. International Journal of Computational Intelligence and Applications, 2008, 07, 429-446.	0.6	4
20	Input Variable Partitioning Method for Functional Decomposition of Functions Specified by Large Truth Tables. , 2007, , .		3
21	Interconnection Networks Efficiency in System-on-Chip Distributed Computing System: Concentrated Mesh and Fat Tree. , 2017, , .		2
22	Location of Processor Allocator and Job Scheduler and Its Impact on CMP Performance. International Journal of Electronics and Telecommunications, 2012, 58, 9-14.	0.5	2
23	Improved Genetic Algorithm for Finite-Horizon Optimal Control of Nonlinear Systems. , 2017, , .		1
24	Multiple voltage synthesis scheme for low power design under timing and resource constraints. Integrated Computer-Aided Engineering, 2005, 12, 369-378.	2.5	0
25	Scheduling and Partitioning Schemes for Low Power Designs Using Multiple Supply Voltages. Journal of Supercomputing, 2006, 35, 93-113.	2.4	0
26	A Graph-Based Approach to Symbolic Functional Decomposition of Finite State Machines. , 2008, , .		0
27	Ear-Slicing and Quality Triangulation. , 2011, , .		0
28	Extended Analysis of Resource Assignment in Modern Chip Multiprocessors. , 2011, , .		0
29	Power usage optimization in multi-UAV common-mission cooperative UAS systems. , 2018, , .		0