

# Dietmar Tutsch

## List of Publications by Year in descending order

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Version: 2024-02-01

17  
papers

108  
citations

1937685

4  
h-index

1872680

6  
g-index

17  
all docs

17  
docs citations

17  
times ranked

65  
citing authors

#	ARTICLE	IF	CITATIONS
1	Conceptual Evaluation and Comparison of A Data Retaining Multiplexer. , 2022, , .		0
2	A New Concept for Multiplexers in Interconnect Blocks of FPGAs. , 2021, , .		0
3	Low-power Characteristics and Data Retention Abilities of A New Multiplexer Logic for Interconnect Logic of FPGAs. , 2021, , .		0
4	Anode Effect Prediction in Hall-H $\ddot{a}$ roult Cells Using Time Series Characteristics. Applied Sciences (Switzerland), 2020, 10, 9050.	2.5	1
5	Low-power Concepts for FPGAs in Applications with limited Energy Resources. , 2020, , .		0
6	PLDAD $\hat{e}$ ”An Algorihm to Reduce Data Center Energy Consumption. Energies, 2018, 11, 2821.	3.1	4
7	A new approach to mapping software to coprocessor circuits. , 2016, , .		0
8	A DTMC Model for Performance Evaluation of Irregular Interconnection Networks with Asymmetric Spatial Traffic Distributions. Lecture Notes in Computer Science, 2016, , 193-209.	1.3	0
9	A Criteria Transformation Approach to Pattern Matching based on Non-Linear Parameter Optimization. Journal of Intelligent Systems, 2015, 24, 249-263.	1.6	1
10	A probabilistic approach to pattern-matching based on non-linear parameter optimization. , 2014, , .		0
11	An Integrated Modeling Approach to Evaluate and Optimize Data Center Sustainability, Dependability and Cost. Energies, 2014, 7, 238-277.	3.1	32
12	Estimating sustainability impact of high dependable data centers: a comparative study between Brazilian and US energy mixes. Computing (Vienna/New York), 2013, 95, 1137-1170.	4.8	24
13	Models for dependability and sustainability analysis of data center cooling architectures. , 2012, , .		19
14	Reconfigurable parallel computing. , 2010, , .		0
15	An Analyzable On-Chip Network Architecture for Embedded Systems. , 2006, , 63-72.		0
16	Generating Systems of Equations for Performance Evaluation of Multistage Interconnection Networks. Journal of Parallel and Distributed Computing, 2002, 62, 228-240.	4.1	27
17	Buffer Design in Delta Networks. , 2002, , 93-101.		0