

Ani Nahapetian

List of Publications by Year in descending order

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Version: 2024-02-01

21
papers

363
citations

1684188

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1720034

7
g-index

21
all docs

21
docs citations

21
times ranked

284
citing authors

#	ARTICLE	IF	CITATIONS
1	Sensing the Neck: Classifying Movements and Actions with a Neck-Mounted Wearable Device. <i>Sensors</i> , 2022, 22, 4313.	3.8	3
2	Look Ma, No Hands. , 2021, , .		1
3	Bluetooth-based context modeling. , 2018, , .		4
4	LuxLeak. , 2016, , .		13
5	Edemeter: Wearable and continuous fluid retention monitoring. , 2016, , .		2
6	AirDraw: Leveraging smart watch motion sensors for mobile human computer interactions. , 2016, , .		31
7	Side-channel attacks on mobile and wearable systems. , 2016, , .		18
8	LightVest: A Wearable Body Position Monitor Using Ambient and Infrared Light. , 2014, , .		6
9	Incorporating development for mobile devices when teaching software engineering. , 2013, , .		0
10	Quantitative Intellectual Property Protection Using Physical-Level Characterization. <i>IEEE Transactions on Information Forensics and Security</i> , 2013, 8, 1722-1730.	6.9	5
11	A mechanism for data quality estimation of on-body cardiac sensor networks. , 2012, , .		3
12	Machine Learning-Based Adaptive Wireless Interval Training Guidance System. <i>Mobile Networks and Applications</i> , 2012, 17, 163-177.	3.3	8
13	Gate Characterization Using Singular Value Decomposition: Foundations and Applications. <i>IEEE Transactions on Information Forensics and Security</i> , 2012, 7, 765-773.	6.9	15
14	Data Fusion for Movement Visualization in a Remote Health Monitoring System. <i>Lecture Notes of the Institute for Computer Sciences, Social-Informatics and Telecommunications Engineering</i> , 2012, , 32-40.	0.3	2
15	Robust passive hardware metering. , 2011, , .		21
16	Hardware Trojan horse detection using gate-level characterization. , 2009, , .		183
17	Interval training guidance system with music and wireless group exercise motivations. , 2009, , .		3
18	General Methodology for Soft-Error-Aware Power Optimization Using Gate Sizing. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2008, 27, 1788-1797.	2.7	20

#	ARTICLE	IF	CITATIONS
19	System Level Estimation of Interconnect Length in the Presence of IP Blocks. , 2007, , .		3
20	Dynamic Reconfiguration in Sensor Networks with Regenerative Energy Sources. , 2007, , .		11
21	Collaborative and Reconfigurable Object Tracking. Journal of Supercomputing, 2004, 30, 213-238.	3.6	11