

# Ayman Fayed

## List of Publications by Year in descending order

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10  
papers

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citations

2258059

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2272923

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citing authors

#	ARTICLE	IF	CITATIONS
1	A Low-Power Dual-Frequency SIMO Buck Converter Topology With Fully-Integrated Outputs and Fast Dynamic Operation in 45 nm CMOS. IEEE Journal of Solid-State Circuits, 2015, 50, 2161-2173.	5.4	35
2	A 1 A, Dual-Inductor 4-Output Buck Converter With 20 MHz/100 MHz Dual-Frequency Switching and Integrated Output Filters in 65 nm CMOS. IEEE Journal of Solid-State Circuits, 2016, 51, 2485-2500.	5.4	26
3	A feasibility study of high-frequency buck regulators in nanometer CMOS technologies. , 2009, , .		12
4	Output Control Techniques for Dual-Frequency SIMO Buck Converters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 4055-4067.	5.4	7
5	Dual-frequency SIMO power converters for low-power on-chip power grids in SoCs. , 2014, , .		6
6	Power conversion schemes in nanometer CMOS mixed-signal SoCs. , 2014, , .		4
7	On-chip input and ground ringing suppression in high-frequency buck converters. , 2015, , .		2
8	An adaptive high-voltage protection and common-mode adjustment circuit for passive&#x2013;active continuous-time sigma&#x2013;delta ADCs. Analog Integrated Circuits and Signal Processing, 2010, 62, 159-166.	1.4	1
9	A Dual-Frequency Single-Inductor Single-Output DC-DC Converter Topology with Spur-Free Switching for Security Applications. , 2021, , .		1
10	A 6mW 480MHz continuous time &#x03A3;&#x0394; modulator with 65dB DR over 5MHz bandwidth in 65nm CMOS. , 2010, , .		0