Jie Kuang

List of Publications by Year
in descending order

15 papers	160 citations	1478505 6 h-index	1474206 9 g-index
15 all docs	15 docs citations	15 times ranked	89 citing authors

#	Article	IF	Citations
1	A 3.9-ps RMS Precision Time-to-Digital Converter Using Ones-Counter Encoding Scheme in a Kintex-7 FPGA. IEEE Transactions on Nuclear Science, 2017, 64, 2713-2718.	2.0	68
2	A 3.0-ps rms Precision 277-MSamples/s Throughput Time-to-Digital Converter Using Multi-Edge Encoding Scheme in a Kintex-7 FPGA. IEEE Transactions on Nuclear Science, 2019, 66, 2275-2281.	2.0	32
3	Implementation of a high precision multi-measurement time-to-digital convertor on a Kintex-7 FPGA. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2018, 891, 37-41.	1.6	15
4	Implementation of 5.3 ps RMS precision and 350 M samples/second throughput time-to-digital converters with event sampling architecture in a Kintex-7 FPGA. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2019, 944, 162584.	1.6	7
5	True random number generation based on arrival time and position of dark counts in a multichannel silicon photomultiplier. Review of Scientific Instruments, 2019, 90, 114704.	1.3	6
6	A Clock Distribution and Synchronization Scheme Over Optical Links for Large-Scale Physics Experiments. IEEE Transactions on Nuclear Science, 2021, 68, 1351-1358.	2.0	6
7	Performance analysis and IP Core Implementation of two high performance time-to-digital converters on Xilinx 7-series FPGA. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2021, 1020, 165866.	1.6	5
8	Performance Evaluation of Time Distribution over SerDes-based Interconnections for PET System. , 2018, , .		4
9	Comparison of Three Pre-Amplifier Circuits for Time Readout of SiPM in TOF-PET Detectors. , 2019, , .		4
10	An FPGA-Based Fast Linear Discharge Readout Scheme Enabling Simultaneous Time and Energy Measurements for TOF-PET Detectors. IEEE Transactions on Radiation and Plasma Medical Sciences, 2020, 4, 30-36.	3.7	4
11	An FPGA-based Time Sampling Charge Measurement Method for TOF-PET Detectors. , 2019, , .		3
12	A time-driven FPGA-based fast nuclear charge digitization method. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2019, 946, 162666.	1.6	2
13	A Resource-saving Method for Implementation of High-Performance Time-to-Digital Converters in FPGA. , 2020, , .		2
14	A 128-Channel High Performance Time-to-Digital Converter Implemented in an UltraScale FPGA. , 2017, , .		1
15	Performance Evaluation of a Clock Synchronization over Fiber Data links for Large Experiments. , 2019, , .		1