Mustafa Berke Yelten

List of Publications by Year
in descending order

57 papers	451 citations	1040056 9 h-index	794594 19 g-index
57 all docs	57 docs citations	57 times ranked	316 citing authors

#	Article	IF	Citations
1	Demystifying Surrogate Modeling for Circuits and Systems. IEEE Circuits and Systems Magazine, 2012, 12, 45-63.	2.3	133
2	Surrogate-Model-Based Analysis of Analog Circuitsâ€"Part I: Variability Analysis. IEEE Transactions on Device and Materials Reliability, 2011, 11, 458-465.	2.0	31
3	Design of Cryogenic LNAs for High Linearity in Space Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 4619-4627.	5.4	26
4	Surrogate-Model-Based Analysis of Analog Circuitsâ€"Part II: Reliability Analysis. IEEE Transactions on Device and Materials Reliability, 2011, 11, 466-473.	2.0	21
5	Silver nanowire coated knitted wool fabrics for wearable electronic applications. Journal of Engineered Fibers and Fabrics, 2019, 14, 155892501985622.	1.0	21
6	Statistical MOSFET Modeling Methodology for Cryogenic Conditions. IEEE Transactions on Electron Devices, 2019, 66, 66-72.	3.0	21
7	Modeling of Total Ionizing Dose Degradation on 180-nm n-MOSFETs Using BSIM3. IEEE Transactions on Electron Devices, 2019, 66, 4617-4622.	3.0	15
8	Review: Analog design methodologies for reliability in nanoscale CMOS circuits. , 2017, , .		14
9	A 180-nm X-Band Cryogenic CMOS LNA. IEEE Microwave and Wireless Components Letters, 2020, 30, 395-398.	3.2	12
10	Experimental and modeling studies of automotive-qualified OLEDs under electrical stress. Microelectronics Reliability, 2020, 111, 113704.	1.7	11
11	Design of a tunable LNA and its variability analysis through surrogate modeling. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2020, 33, e2724.	1.9	10
12	Comparison of modeling techniques in circuit variability analysis. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2012, 25, 288-302.	1.9	9
13	A cryogenic modeling methodology of MOSFET I-V characteristics in BSIM3. , 2017, , .		8
14	A High Performance TIA Design in 40 nm CMOS. , 2020, , .		7
15	Theoretical analysis and characterization of the tunable matching networks in low noise amplifiers. , 2009, , .		6
16	A novel design procedure for tunable low noise amplifiers. , 2009, , .		6
17	Analog Negative-Bias-Temperature-Instability Monitoring Circuit. IEEE Transactions on Device and Materials Reliability, 2012, 12, 177-179.	2.0	6
18	Reliability Testing of 3D-Printed Electromechanical Scanning Devices. Journal of Electronic Testing: Theory and Applications (JETTA), 2018, 34, 363-370.	1.2	6

#	Article	IF	Citations
19	A Cryogenic LC VCO Utilizing Cryogenic Models of Active Devices. , 2019, , .		6
20	An ISM-Band Multi-Phase Injection-Locked Ring Oscillator. , 2021, , .		6
21	An Automated Setup for the Characterization of Time-Based Degradation Effects Including the Process Variability in 40-nm CMOS Transistors. IEEE Transactions on Instrumentation and Measurement, 2021, 70, 1-10.	4.7	6
22	A 0.18 μm CMOS X-Band Low Noise Amplifier for Space Applications. , 2017, , .		5
23	Surrogate modeling and variability analysis of on–chip spiral inductors. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2018, 31, e2313.	1.9	5
24	Reliability Testing of 3D-Printed Polyamide Actuators. IEEE Transactions on Device and Materials Reliability, 2020, 20, 152-156.	2.0	5
25	Time-dependent dielectric breakdown (TDDB) reliability analysis of CMOS analog and radio frequency (RF) circuits. Analog Integrated Circuits and Signal Processing, 2018, 97, 39-47.	1.4	4
26	A Novel Multiple Membership Function Generator for Fuzzy Logic Systems. , 2018, , .		4
27	Variability-aware cryogenic models of mosfets: validation and circuit design. Semiconductor Science and Technology, 2019, 34, 115004.	2.0	4
28	Radiation tolerance impact of trap density near the drain and source regions of a MOSFET. Nuclear Instruments & Methods in Physics Research B, 2019, 449, 1-5.	1.4	4
29	Design and validation of an artificial neural network based on analog circuits. Analog Integrated Circuits and Signal Processing, 2021, 106, 475-483.	1.4	4
30	Holistic Device Modeling: Toward a Unified MOSFET Model Including Variability, Aging, and Extreme Operating Conditions. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 2635-2640.	3.0	4
31	A High Speed 180 NM CMOS Cryogenic SAR ADC. , 2018, , .		3
32	A Rare Event Based Yield Estimation Methodology for Analog Circuits. , 2018, , .		3
33	A wide-temperature range (77–400 K) CMOS low-dropout voltage regulator system. Analog Integrated Circuits and Signal Processing, 2021, 106, 501-510.	1.4	3
34	Process mismatch analysis based on reduced-order models. , 2012, , .		2
35	Efficient signature selection tool for sense & react systems. , 2016, , .		2
36	A high linearity LNA using 180 nm CMOS technology for S-Band. , 2017, , .		2

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37	On Chip Reconfigurable CMOS Analog Circuit Design and Automation Against Aging Phenomena. ACM Transactions on Design Automation of Electronic Systems, 2019, 24, 1-22.	2.6	2
38	Design of a LC Voltage-Controlled Oscillator for Space Applications in C-Band. , 2019, , .		2
39	A Cryogenic CMOS Low Dropout Voltage Regulator Design for Space Applications. , 2019, , .		2
40	A switchable DC offset cancellation circuit for time-based degradation correction. Analog Integrated Circuits and Signal Processing, 2021, 106, 485-491.	1.4	2
41	Cryogenic DC Characteristics of Low Threshold Voltage (VTH) n-channel MOSFETs. Balkan Journal of Electrical and Computer Engineering, 2019, 7, 362-365.	0.6	2
42	Scalable and efficient analog parametric fault identification. , 2013, , .		1
43	Comparison of ELTs with different shapes and a regular layout transistor in 180 nm CMOS process. , 2019, , .		1
44	An Offset Cancellation Set-up for Amplifiers Subject to Aging. , 2019, , .		1
45	Design of An Analog Circuit-Based Artificial Neural Network. , 2019, , .		1
46	Monitoring modal shape of miniaturized dynamic structures via laser triangulation and stroboscopy. Microsystem Technologies, 2021, 27, 3751-3756.	2.0	1
47	A Simulation Tool for Space Applications: RadiSPICE. , 2022, , .		1
48	Model-Based Variation-Aware Integrated Circuit Design. , 2013, , 171-188.		0
49	A heuristic sensitivity analysis technique for high-dimensional systems. , 2016, , .		0
50	Aging signature properties and an efficient signature determination tool for online monitoring. The Integration VLSI Journal, 2017, 58, 496-503.	2.1	0
51	Reliability of 3D-printed dynamic scanners. , 2017, , .		0
52	Design of Logic Gates by Using a Four-Gate Thin Film Transistor (FG TFT). , 2018, , .		0
53	Hysteretic Buck-Boost Converter for Wearable Applications. , 2019, , .		0
54	Variation-Aware Circuit Macromodeling and Design Based on Surrogate Models. Advances in Intelligent Systems and Computing, 2013, , 255-269.	0.6	0

#	Article	IF	Citations
55	Gamma Ray Effects on Organic Light-Emitting Diodes (OLEDs). , 2021, , .		0
56	Ultra Low Power Transimpedance Amplifier Design for Receivers with Large-Area Photodetectors. , 2021, , .		0
57	Responsivity Comparison of Different Photodiode Structures. , 2022, , .		0