

# Bongjin Kim

## List of Publications by Year in descending order

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10  
papers

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1684188

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docs citations

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citing authors

#	ARTICLE	IF	CITATIONS
1	Reverse Rate Matching for Low-Power LTE-Advanced Turbo Decoders. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 2920-2928.	5.4	7
2	Low-Complexity Tree Architecture for Finding the First Two Minima. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 61-64.	3.0	29
3	Tail-Overlapped SISO Decoding for High-Throughput LTE-Advanced Turbo Decoders. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 2711-2720.	5.4	7
4	Low-Complexity Low-Latency Architecture for Matching of Data Encoded With Hard Systematic Error-Correcting Codes. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 1648-1652.	3.1	6
5	Low-Complexity Parallel QPP Interleaver Based on Permutation Patterns. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 162-166.	3.0	6
6	Memory-Optimized Hybrid Decoding Method for Multi-Rate Turbo Codes. , 2013, , .		1
7	Area-Efficient QC-LDPC Decoder Architecture Based on Stride Scheduling and Memory Bank Division. IEICE Transactions on Communications, 2013, E96.B, 1772-1779.	0.7	2
8	Immediate Exchange of Extrinsic Information for High-Throughput Turbo Decoding. IEEE Communications Letters, 2012, 16, 2048-2051.	4.1	0
9	QC-LDPC Decoding Architecture based on Stride Scheduling. , 2011, , .		1
10	Dual-rail decoding of low-density parity-check codes. , 2010, , .		0