Oreste Villa

List of Publications by Year
in descending order

18 papers	**471** citations	1040056 **9** h-index	1125743 **13** g-index
19 all docs	**19** docs citations	**19** times ranked	**477** citing authors

#	Article	IF	Citations
1	GPU-Based Implementations of the Noniterative Regularized-CCSD(T) Corrections: Applications to Strongly Correlated Systems. Journal of Chemical Theory and Computation, 2011, 7, 1316-1327.	5.3	72
2	Active-space completely-renormalized equation-of-motion coupled-cluster formalism: Excited-state studies of green fluorescent protein, free-base porphyrin, and oligoporphyrin dimer. Journal of Chemical Physics, 2010, 132, 154103.	3.0	59
3	Efficient Breadth-First Search on the Cell/BE Processor. IEEE Transactions on Parallel and Distributed Systems, 2008, 19, 1381-1395.	5.6	51
4	Noniterative Multireference Coupled Cluster Methods on Heterogeneous CPU–GPU Systems. Journal of Chemical Theory and Computation, 2013, 9, 1949-1957.	5.3	37
5	MCM-GPU. Computer Architecture News, 2017, 45, 320-332.	2.5	34
6	Aho-Corasick String Matching on Shared and Distributed-Memory Parallel Architectures. IEEE Transactions on Parallel and Distributed Systems, 2012, 23, 436-443.	5.6	32
7	Optimizing tensor contraction expressions for hybrid CPU-GPU execution. Cluster Computing, 2013, 16, 131-155.	5.0	32
8	Input-independent, scalable and fast string matching on the Cray XMT. , 2009, , .		30
9	Exploration of distributed shared memory architectures for NoC-based multiprocessors. Journal of Systems Architecture, 2007, 53, 719-732.	4.3	27
10	Exploration of Distributed Shared Memory Architectures for NoC-based Multiprocessors. , 2006, , .		22
11	Scaling Irregular Applications through Data Aggregation and Software Multithreading. , 2014, , .		19
12	Exploring Fine-Grained Task-Based Execution on Multi-GPU Systems. , 2011, , .		18
13	Scaling Semantic Graph Databases in Size and Performance. IEEE Micro, 2014, 34, 16-26.	1.8	13
14	Fast and Accurate Simulation of the Cray XMT Multithreaded Supercomputer. IEEE Transactions on Parallel and Distributed Systems, 2012, 23, 2266-2279.	5.6	7
15	Designing Next-Generation Massively Multithreaded Architectures for Irregular Applications. Computer, 2012, 45, 53-61.	1.1	5
16	Exploring Efficient Hardware Support for Applications with Irregular Memory Patterns on Multinode Manycore Architectures. IEEE Transactions on Parallel and Distributed Systems, 2017, 28, 1635-1648.	5.6	4
17	Contention Modeling for Multithreaded Distributed Shared Memory Machines: The Cray XMT. , 2011, , .		3
18	Toward a data scalable solution for facilitating discovery of science resources. Parallel Computing, 2014, 40, 682-696.	2.1	3