

Arpan Dasgupta

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Reliability analysis through linearity and harmonic distortion of a dual-material-gate asymmetric underlapped DGMOSFET. <i>Microelectronics Reliability</i> , 2019, 92, 106-113.	1.7	2
2	A linearity based comparison between symmetric and asymmetric lateral diffusion for a 22Ånm Underlapped DG-MOSFET. <i>Superlattices and Microstructures</i> , 2017, 107, 69-82.	3.1	6
3	Effect of spacer dielectric engineering on Asymmetric Source Underlapped Double Gate MOSFET using Gate Stack. <i>Superlattices and Microstructures</i> , 2017, 101, 87-95.	3.1	5
4	Effect of channel engineering on analog/RF performance of underlapped gatestack DG-MOSFET in Sub-20nm regime. , 2017, , .		3
5	Influence of Underlap on Gate Stack DG-MOSFET for analytical study of Analog/RF performance. <i>Superlattices and Microstructures</i> , 2016, 94, 60-73.	3.1	33
6	Impact of Asymmetric Dual-k Spacer in the Underlap Regions of Sub 20Ånm NMOSFET with Gate Stack. <i>Superlattices and Microstructures</i> , 2016, 98, 448-457.	3.1	4
7	Impact of Lateral Straggle on the Analog/RF Performance of Asymmetric Gate Stack Double Gate MOSFET. <i>Superlattices and Microstructures</i> , 2016, 97, 477-488.	3.1	7
8	Analysis of high-k spacer on symmetric underlap DG-MOSFET with Gate Stack architecture. <i>Superlattices and Microstructures</i> , 2016, 97, 386-396.	3.1	12