PÃ©ter Szolgay

List of Publications by Year
in descending order

31 papers	233 citations	1477746 6 h-index	1372195 10 g-index
31 all docs	31 docs citations	31 times ranked	147 citing authors

#	Article	IF	Citations
1	Accelerating unstructured finite volume computations on field‐programmable gate arrays. Concurrency Computation Practice and Experience, 2014, 26, 615-643.	1.4	5
2	Examining the accuracy and the precision of PDEs for FPGA computations. , 2012, , .		0
3	FPGA based acceleration of computational fluid flow simulation on unstructured mesh geometry. , 2012, , .		10
4	Analysis of a GPU based CNN implementation. , 2012, , .		11
5	Cellular processor array based UAV safety system. , 2012, , .		0
6	A CNN motivated array computing model. , 2010, , .		0
7	Variational computing based segmentation methods for medical imaging by using CNN. , 2010, , .		11
8	CNN model on stream processing platform. , 2009, , .		2
9	Variational computing based images denoising methods by using cellular neural networks. , 2009, , .		3
10	Implementation of a parallel SAD based wavefront sensor architecture on FPGA. , 2009, , .		2
11	Simulation of 2D inviscid, adiabatic, compressible flows on emulated digital CNN‐UM. International Journal of Circuit Theory and Applications, 2009, 37, 569-585.	1.3	11
12	Simulation of Two-Dimensional Supersonic Flows on Emulated-Digital CNN-UM. Eurasip Journal on Advances in Signal Processing, 2009, 2009, .	1.0	1
13	Implementation of embedded emulated‐digital CNN‐UM global analogic programming unit on FPGA and its application. International Journal of Circuit Theory and Applications, 2008, 36, 589-603.	1.3	27
14	Toward exploitation of cell multi‐processor array in time‐consuming applications by using CNN model. International Journal of Circuit Theory and Applications, 2008, 36, 605-622.	1.3	6
15	FPGA based emulated-digital CNN-UM implementation with GAPU. , 2008, , .		3
16	A standalone FPGA based emulated-digital CNN-UM system. , 2008, , .		2
17	An advanced real-time, multi-channel emulated-digital retina model implementation on FPGA. , 2008, , .		1
18	Experimental Result on Supersonic Flow Simulation on Emulated Digital CNN-UM. , 2008, , .		0

#	Article	IF	Citations
19	Emulated digital CNN-UM on different kind of array processors. , 2008, , .		1
20	Toward exploitation of cell multi-processor array in time-consuming applications by using CNN model. , 2008, , .		2
21	Two-dimensional Compressible Flow Simulation on Emulated Digital CNN-UM. , 2008, , .		14
22	CNN-UM based transversely isotropic elastic wave propagation simulation. , 2007, , .		2
23	CNN model on cell multiprocessor array. , 2007, , .		5
24	Emulated digital CNN solution for two dimensional compressible flows. , 2007, , .		2
25	CNNUM-Based Methods Using Deformable Contours on Smooth Boundaries. , 2006, , .		1
26	Emulated digital CNN-UM solution of partial differential equations. International Journal of Circuit Theory and Applications, 2006, 34, 445-470.	1.3	37
27	3D tactile sensor array processed by CNN-UM: a fast method for detecting and identifying slippage and twisting motion. International Journal of Circuit Theory and Applications, 2006, 34, 517-531.	1.3	22
28	Some new analogic CNN algorithms for PCB quality control. International Journal of Circuit Theory and Applications, 2002, 30, 231-245.	1.3	1
29	An Emulated Digital CNN Implementation. Journal of Signal Processing Systems, 1999, 23, 291-303.	1.0	39
30	Analogic algorithms for optical detection of breaks and short circuits on the layouts of printed circuit boards using CNN. International Journal of Circuit Theory and Applications, 1999, 27, 103-116.	1.3	10
31	On algorithms in the parallel design of logic and layout of circuits with functional blocks. International Journal of Circuit Theory and Applications, 1992, 20, 411-429.	1.3	2