

# Pter Szolgay

## List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

26  
papers

166  
citations

7  
h-index

12  
g-index

31  
ext. papers

201  
ext. citations

1.8  
avg, IF

2.15  
L-index

#	Paper	IF	Citations
26	An Emulated Digital CNN Implementation. <i>Journal of Signal Processing Systems</i> , <b>1999</b> , 23, 291-303		29
25	Emulated digital CNN-UM solution of partial differential equations. <i>International Journal of Circuit Theory and Applications</i> , <b>2006</b> , 34, 445-470	2	26
24	Implementation of embedded emulated-digital CNN-UM global analogic programming unit on FPGA and its application. <i>International Journal of Circuit Theory and Applications</i> , <b>2008</b> , 36, 589-603	2	23
23	3D tactile sensor array processed by CNN-UM: a fast method for detecting and identifying slippage and twisting motion. <i>International Journal of Circuit Theory and Applications</i> , <b>2006</b> , 34, 517-531	2	17
22	Analysis of a GPU based CNN implementation <b>2012</b> ,		9
21	Variational computing based segmentation methods for medical imaging by using CNN <b>2010</b> ,		8
20	Two-dimensional Compressible Flow Simulation on Emulated Digital CNN-UM <b>2008</b> ,		8
19	Simulation of 2D inviscid, adiabatic, compressible flows on emulated digital CNN-UM. <i>International Journal of Circuit Theory and Applications</i> , <b>2009</b> , 37, 569-585	2	7
18	Analogic algorithms for optical detection of breaks and short circuits on the layouts of printed circuit boards using CNN. <i>International Journal of Circuit Theory and Applications</i> , <b>1999</b> , 27, 103-116	2	7
17	FPGA based acceleration of computational fluid flow simulation on unstructured mesh geometry <b>2012</b> ,		6
16	Accelerating unstructured finite volume computations on field-programmable gate arrays. <i>Concurrency Computation Practice and Experience</i> , <b>2014</b> , 26, 615-643	1.4	4
15	Toward exploitation of cell multi-processor array in time-consuming applications by using CNN model. <i>International Journal of Circuit Theory and Applications</i> , <b>2008</b> , 36, 605-622	2	4
14	Variational computing based images denoising methods by using cellular neural networks <b>2009</b> ,		2
13	FPGA based emulated-digital CNN-UM implementation with GAPI <b>2008</b> ,		2
12	CNN-UM based transversely isotropic elastic wave propagation simulation <b>2007</b> ,		2
11	CNN model on cell multiprocessor array <b>2007</b> ,		2
10	On algorithms in the parallel design of logic and layout of circuits with functional blocks. <i>International Journal of Circuit Theory and Applications</i> , <b>1992</b> , 20, 411-429	2	2

9	CNN model on stream processing platform <b>2009</b> ,		1
8	Implementation of a parallel SAD based wavefront sensor architecture on FPGA <b>2009</b> ,		1
7	Simulation of Two-Dimensional Supersonic Flows on Emulated-Digital CNN-UM. <i>Eurasip Journal on Advances in Signal Processing</i> , <b>2009</b> , 2009,	1.9	1
6	A standalone FPGA based emulated-digital CNN-UM system <b>2008</b> ,		1
5	An advanced real-time, multi-channel emulated-digital retina model implementation on FPGA <b>2008</b> ,		1
4	Emulated digital CNN-UM on different kind of array processors <b>2008</b> ,		1
3	Emulated digital CNN solution for two dimensional compressible flows <b>2007</b> ,		1
2	CNNUM-Based Methods Using Deformable Contours on Smooth Boundaries <b>2006</b> ,		1
1	Some new analogic CNN algorithms for PCB quality control. <i>International Journal of Circuit Theory and Applications</i> , <b>2002</b> , 30, 231-245		2