

# Kshitij Auluck

## List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

11  
papers

59  
citations

5  
h-index

7  
g-index

16  
ext. papers

64  
ext. citations

2.8  
avg, IF

1.45  
L-index

#	Paper	IF	Citations
11	Critical Assessment on Modeling and Design of Nonfaradaic CMOS Electrochemical Sensing. <i>IEEE Sensors Journal</i> , <b>2016</b> , 16, 3367-3373	4	
10	Circuit Models for Ferroelectrics Part I: Physics of Polarization Switching. <i>IEEE Transactions on Electron Devices</i> , <b>2016</b> , 63, 631-636	2.9	3
9	Circuit Models for Ferroelectrics Part II: Analysis of FE-Nonvolatile Latches. <i>IEEE Transactions on Electron Devices</i> , <b>2016</b> , 63, 637-642	2.9	4
8	Programmable ion-sensitive transistor interfaces. III. Design considerations, signal generation, and sensitivity enhancement. <i>Physical Review E</i> , <b>2014</b> , 89, 052817	2.4	13
7	A unified circuit model for ferroelectrics <b>2014</b> ,		1
6	Ferroelectric-Assisted Dual-Switching Speed DRAM Flash Hybrid Memory. <i>IEEE Transactions on Electron Devices</i> , <b>2013</b> , 60, 1944-1950	2.9	7
5	. <i>IEEE Transactions on Electron Devices</i> , <b>2013</b> , 60, 3378-3384	2.9	2
4	. <i>IEEE Transactions on Electron Devices</i> , <b>2012</b> , 59, 441-449	2.9	8
3	. <i>IEEE Transactions on Electron Devices</i> , <b>2012</b> , 59, 450-458	2.9	8
2	Switching dynamics in ferroelectric-charge hybrid nonvolatile memory <b>2012</b> ,		1
1	Performance and Reliability Study of Single-Layer and Dual-Layer Platinum Nanocrystal Flash Memory Devices Under NAND Operation. <i>IEEE Transactions on Electron Devices</i> , <b>2010</b> , 57, 1829-1837	2.9	11