

Kshitij Auluck

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

11
papers

59
citations

5
h-index

7
g-index

16
ext. papers

64
ext. citations

2.8
avg, IF

1.45
L-index

#	Paper	IF	Citations
11	Programmable ion-sensitive transistor interfaces. III. Design considerations, signal generation, and sensitivity enhancement. <i>Physical Review E</i> , 2014 , 89, 052817	2.4	13
10	Performance and Reliability Study of Single-Layer and Dual-Layer Platinum Nanocrystal Flash Memory Devices Under NAND Operation. <i>IEEE Transactions on Electron Devices</i> , 2010 , 57, 1829-1837	2.9	11
9	. <i>IEEE Transactions on Electron Devices</i> , 2012 , 59, 441-449	2.9	8
8	. <i>IEEE Transactions on Electron Devices</i> , 2012 , 59, 450-458	2.9	8
7	Ferroelectric-Assisted Dual-Switching Speed DRAM Flash Hybrid Memory. <i>IEEE Transactions on Electron Devices</i> , 2013 , 60, 1944-1950	2.9	7
6	Circuit Models for Ferroelectrics Part II: Analysis of FE-Nonvolatile Latches. <i>IEEE Transactions on Electron Devices</i> , 2016 , 63, 637-642	2.9	4
5	Circuit Models for Ferroelectrics Part I: Physics of Polarization Switching. <i>IEEE Transactions on Electron Devices</i> , 2016 , 63, 631-636	2.9	3
4	. <i>IEEE Transactions on Electron Devices</i> , 2013 , 60, 3378-3384	2.9	2
3	A unified circuit model for ferroelectrics 2014 ,		1
2	Switching dynamics in ferroelectric-charge hybrid nonvolatile memory 2012 ,		1
1	Critical Assessment on Modeling and Design of Nonfaradaic CMOS Electrochemical Sensing. <i>IEEE Sensors Journal</i> , 2016 , 16, 3367-3373	4	