

# Mark Horowitz

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11758507/publications.pdf>

Version: 2024-02-01

24  
papers

2,284  
citations

840776

11  
h-index

839539

18  
g-index

24  
all docs

24  
docs citations

24  
times ranked

1798  
citing authors

#	ARTICLE	IF	CITATIONS
1	Compiling Algorithms for Heterogeneous Systems. Synthesis Lectures on Computer Architecture, 2018, 13, 1-105.	1.3	3
2	Dark Memory and Accelerator-Rich System Optimization in the Dark Silicon Era. IEEE Design and Test, 2017, 34, 39-50.	1.2	93
3	1.1 Computing's energy problem (and what we can do about it). , 2014, , .		752
4	Forwarding metamorphosis. , 2013, , .		439
5	Forwarding metamorphosis. Computer Communication Review, 2013, 43, 99-110.	1.8	318
6	CPU DB. Communications of the ACM, 2012, 55, 55-63.	4.5	180
7	Energy-performance tradeoffs in processor architecture and circuit design. Computer Architecture News, 2010, 38, 26-36.	2.5	32
8	Intent-leveraged optimization of analog circuits via homotopy. , 2010, , .		0
9	Robust Energy-Efficient Adder Topologies. Computer Arithmetic, IEEE Symposium on, 2007, , .	0.0	40
10	Scaling, Power and the Future of CMOS. , 2007, , .		16
11	Architectural support for copy and tamper resistant software. Operating Systems Review (ACM), 2000, 34, 168-177.	1.9	31
12	FLASH vs. (simulated) FLASH. ACM SIGPLAN Notices, 2000, 35, 49-58.	0.2	35
13	FLASH vs. (Simulated) FLASH. Operating Systems Review (ACM), 2000, 34, 49-58.	1.9	5
14	Architectural support for copy and tamper resistant software. Computer Architecture News, 2000, 28, 168-177.	2.5	132
15	Architectural support for copy and tamper resistant software. ACM SIGPLAN Notices, 2000, 35, 168-177.	0.2	120
16	Smart Memories. Computer Architecture News, 2000, 28, 161-171.	2.5	17
17	FLASH vs. (Simulated) FLASH. Computer Architecture News, 2000, 28, 49-58.	2.5	3
18	Hardware fault containment in scalable shared-memory multiprocessors. Computer Architecture News, 1997, 25, 73-84.	2.5	6

#	ARTICLE	IF	CITATIONS
19	Informing memory operations. Computer Architecture News, 1996, 24, 260-270.	2.5	5
20	Interleaving. Operating Systems Review (ACM), 1994, 28, 308-318.	1.9	6
21	The performance impact of flexibility in the Stanford FLASH multiprocessor. ACM SIGPLAN Notices, 1994, 29, 274-285.	0.2	50
22	The performance impact of flexibility in the Stanford FLASH multiprocessor. Operating Systems Review (ACM), 1994, 28, 274-285.	1.9	0
23	Interleaving. ACM SIGPLAN Notices, 1994, 29, 308-318.	0.2	1
24	Modeling the performance of limited pointers directories for cache coherence. Computer Architecture News, 1991, 19, 309-319.	2.5	0