

# Chris Wilkerson

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11709505/publications.pdf>

Version: 2024-02-01

15  
papers

1,744  
citations

1937685

4  
h-index

2272923

4  
g-index

15  
all docs

15  
docs citations

15  
times ranked

757  
citing authors

#	ARTICLE	IF	CITATIONS
1	Flipping bits in memory without accessing them. Computer Architecture News, 2014, 42, 361-372.	2.5	422
2	Trading off Cache Capacity for Reliability to Enable Low Voltage Operation. , 2008, , .		199
3	An experimental study of data retention behavior in modern DRAM devices. , 2013, , .		166
4	Reducing cache power with low-cost, multi-bit error-correcting codes. , 2010, , .		145
5	Improving DRAM performance by parallelizing refreshes with accesses. , 2014, , .		128
6	Improving cache lifetime reliability at ultra-low voltages. , 2009, , .		126
7	Efficiently prefetching complex address patterns. , 2015, , .		98
8	Energy-efficient cache design using variable-strength error-correcting codes. , 2011, , .		92
9	The efficacy of error mitigation techniques for DRAM retention failures. , 2014, , .		80
10	Path confidence based lookahead prefetching. , 2016, , .		75
11	Sandbox Prefetching: Safe run-time evaluation of aggressive prefetchers. , 2014, , .		70
12	The efficacy of error mitigation techniques for DRAM retention failures. Performance Evaluation Review, 2014, 42, 519-532.	0.6	58
13	Detecting and mitigating data-dependent DRAM failures by exploiting current memory content. , 2017, , .		49
14	Energy-efficient cache design using variable-strength error-correcting codes. Computer Architecture News, 2011, 39, 461-472.	2.5	25
15	Kill the Program Counter. ACM SIGPLAN Notices, 2017, 52, 737-749.	0.2	11