Akihiko Tsukahara

List of Publications by Year
in descending order

Source: https://exaly.com/author-pdf/11698277/publications.pdf

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15	25	2682572	2053705
papers	citations	2	5
		h-index	g-index

15	15	15	9
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Convolutional Neural Network for Octave Illusion Classification. IEEJ Transactions on Electronics, Information and Systems, 2022, 142, 543-549.	0.2	1
2	A design of EEGNetâ€based inference processor for pattern recognition of EEG using FPGA. Electronics and Communications in Japan, 2021, 104, 53-64.	0.5	7
3	Comparing Methods of Feature Extraction of Brain Activities for Octave Illusion Classification Using Machine Learning. Sensors, 2021, 21, 6407.	3.8	1
4	Basic Study on Development of Device Using Strain Gauges to Prevent Dislodgement of a Blood-Access Needle During Hemodialysis Therapy and Its Systemization for Internet of Things. Journal of Life Support Engineering, 2020, 32, 46-52.	0.0	0
5	Design and Trial Production of Stochastic Resonance Processor using FPGA. IEEJ Transactions on Electronics, Information and Systems, 2020, 140, 858-859.	0.2	0
6	A Design of EEGNet based Inference Processor for Pattern Recognition of EEG using FPGA. IEEJ Transactions on Electronics, Information and Systems, 2020, 140, 737-746.	0.2	0
7	Neural Processing of Octave Illusion in Auditory Cortex Revealed by Frequency Tagging Method. IEEJ Transactions on Electronics, Information and Systems, 2020, 140, 762-768.	0.2	0
8	Superconducting Self-shield and Zero Boil-Off MEG Systems. IEEJ Transactions on Electronics, Information and Systems, 2020, 140, 856-857.	0.2	0
9	An Examination of EEG Frequency Components Related to Speech Imagery and Its Identification. IEEJ Transactions on Electronics, Information and Systems, 2019, 139, 588-595.	0.2	1
10	Design of Approximate Arithmetic Circuits within Tolerance. , 2018, , .		0
11	An Architecture of Real Coded Genetic Algorithm Processor. IEEJ Transactions on Electronics, Information and Systems, 2016, 136, 1586-1595.	0.2	0
12	Hardware implementation of evolutionary algorithms using dynamic reconfiguration technology. Natural Computing, 2015, 14, 593-601.	3.0	1
13	Genetic algorithm with dynamic variable number of individuals and accuracy. International Journal of Control, Automation and Systems, 2009, 7, 1-6.	2.7	8
14	Genetic Algorithm that can Dynamically Change Number of Individuals and Accuracy. , 2007, , .		2
15	A Processor for Genetic Algorithm using Dynamically Reconfigurable Memory. , 2006, , .		4