

Akihiko Tsukahara

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11698277/publications.pdf>

Version: 2024-02-01

15
papers

25
citations

2682572

2
h-index

2053705

5
g-index

15
all docs

15
docs citations

15
times ranked

9
citing authors

#	ARTICLE	IF	CITATIONS
1	Genetic algorithm with dynamic variable number of individuals and accuracy. International Journal of Control, Automation and Systems, 2009, 7, 1-6.	2.7	8
2	A design of EEGNet-based inference processor for pattern recognition of EEG using FPGA. Electronics and Communications in Japan, 2021, 104, 53-64.	0.5	7
3	A Processor for Genetic Algorithm using Dynamically Reconfigurable Memory. , 2006, , .		4
4	Genetic Algorithm that can Dynamically Change Number of Individuals and Accuracy. , 2007, , .		2
5	Hardware implementation of evolutionary algorithms using dynamic reconfiguration technology. Natural Computing, 2015, 14, 593-601.	3.0	1
6	Comparing Methods of Feature Extraction of Brain Activities for Octave Illusion Classification Using Machine Learning. Sensors, 2021, 21, 6407.	3.8	1
7	An Examination of EEG Frequency Components Related to Speech Imagery and Its Identification. IEEJ Transactions on Electronics, Information and Systems, 2019, 139, 588-595.	0.2	1
8	Convolutional Neural Network for Octave Illusion Classification. IEEJ Transactions on Electronics, Information and Systems, 2022, 142, 543-549.	0.2	1
9	Design of Approximate Arithmetic Circuits within Tolerance. , 2018, , .		0
10	An Architecture of Real Coded Genetic Algorithm Processor. IEEJ Transactions on Electronics, Information and Systems, 2016, 136, 1586-1595.	0.2	0
11	Basic Study on Development of Device Using Strain Gauges to Prevent Dislodgement of a Blood-Access Needle During Hemodialysis Therapy and Its Systemization for Internet of Things. Journal of Life Support Engineering, 2020, 32, 46-52.	0.0	0
12	Design and Trial Production of Stochastic Resonance Processor using FPGA. IEEJ Transactions on Electronics, Information and Systems, 2020, 140, 858-859.	0.2	0
13	A Design of EEGNet based Inference Processor for Pattern Recognition of EEG using FPGA. IEEJ Transactions on Electronics, Information and Systems, 2020, 140, 737-746.	0.2	0
14	Neural Processing of Octave Illusion in Auditory Cortex Revealed by Frequency Tagging Method. IEEJ Transactions on Electronics, Information and Systems, 2020, 140, 762-768.	0.2	0
15	Superconducting Self-shield and Zero Boil-Off MEG Systems. IEEJ Transactions on Electronics, Information and Systems, 2020, 140, 856-857.	0.2	0