

# David A Wood

## List of Publications by Year in descending order

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86  
papers

9,037  
citations

448610

19  
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182931

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docs citations

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times ranked

4457  
citing authors

#	ARTICLE	IF	CITATIONS
1	Standardized Invasive Hemodynamics for Management of Patients With Elevated Echocardiographic Gradients Post-Transcatheter Aortic Valve Replacement at Midterm Follow-Up. <i>Circulation: Cardiovascular Interventions</i> , 2022, 15, CIRCINTERVENTIONS121011243.	1.4	9
2	Late Balloon Valvuloplasty for Transcatheter Heart Valve Dysfunction. <i>Journal of the American College of Cardiology</i> , 2022, 79, 1340-1351.	1.2	17
3	TAVI in 2022: Remaining issues and future direction. <i>Archives of Cardiovascular Diseases</i> , 2022, 115, 235-242.	0.7	18
4	Cardiovascular Care Delivery During the Second Wave of COVID-19 in Canada. <i>Canadian Journal of Cardiology</i> , 2021, 37, 790-793.	0.8	11
5	Cardiac Rehabilitation During the COVID-19 Era: Guidance on Implementing Virtual Care. <i>Canadian Journal of Cardiology</i> , 2020, 36, 1317-1321.	0.8	68
6	Use of Renin-Angiotensin System Blockers During the COVID-19 Pandemic: Early Guidance and Evolving Evidence. <i>Canadian Journal of Cardiology</i> , 2020, 36, 1180-1182.	0.8	3
7	Guiding Cardiac Care During the COVID-19 Pandemic: How Ethics Shapes Our Health System Response. <i>Canadian Journal of Cardiology</i> , 2020, 36, 1313-1316.	0.8	2
8	A Primer on Memory Consistency and Cache Coherence, Second Edition. <i>Synthesis Lectures on Computer Architecture</i> , 2020, 15, 1-294.	1.3	22
9	Pheochromocytoma-Induced Takotsubo Syndrome Treated With Extracorporeal Membrane Oxygenation. <i>JACC: Case Reports</i> , 2019, 1, 85-90.	0.3	4
10	Aortic Valve-in-Valve in Externally Mounted Bioprosthesis. <i>Innovations: Technology and Techniques in Cardiothoracic and Vascular Surgery</i> , 2018, 13, 171-176.	0.4	4
11	Aortic Valve-in-Valve in Externally Mounted Bioprosthesis. <i>Innovations: Technology and Techniques in Cardiothoracic and Vascular Surgery</i> , 2018, 13, 171-176.	0.4	3
12	LogCA. , 2017, , .		19
13	Upper gastrointestinal bleeding following transcatheter aortic valve replacement: A retrospective analysis. <i>Catheterization and Cardiovascular Interventions</i> , 2017, 90, E53-E61.	0.7	14
14	Crossing Guard. <i>ACM SIGPLAN Notices</i> , 2017, 52, 163-176.	0.2	2
15	Crossing Guard. <i>Computer Architecture News</i> , 2017, 45, 163-176.	2.5	2
16	LogCA. <i>Computer Architecture News</i> , 2017, 45, 375-388.	2.5	9
17	Lazy release consistency for GPUs. , 2016, , .		18
18	The Impact of Left Ventricular Mass on Diastolic Blood Pressure Targets for Patients With Coronary Artery Disease. <i>American Journal of Hypertension</i> , 2016, 29, 1085-1093.	1.0	13

#	ARTICLE	IF	CITATIONS
19	GPGPU Footprint Models to Estimate per-Core Power. IEEE Computer Architecture Letters, 2016, 15, 97-100.	1.0	1
20	Dynamism of the aortic annulus: Effect of diastolic versus systolic CT annular measurements on device selection in transcatheter aortic valve replacement (TAVR). Journal of Cardiovascular Computed Tomography, 2016, 10, 37-43.	0.7	60
21	Yet Another Compressed Cache. Transactions on Architecture and Code Optimization, 2016, 13, 1-25.	1.6	30
22	Border control. , 2015, , .		38
23	GPU Computing Pipeline Inefficiencies and Optimization Opportunities in Heterogeneous CPU-GPU Processors. , 2015, , .		25
24	A Primer on Compression in the Memory Hierarchy. Synthesis Lectures on Computer Architecture, 2015, 10, 1-86.	1.3	13
25	Incidence and Severity of Paravalvular Aortic Regurgitation With Multidetector Computed Tomography Nominal Area Oversizing or Undersizing After Transcatheter Heart Valve Replacement With the Sapien 3. JACC: Cardiovascular Interventions, 2015, 8, 462-471.	1.1	122
26	Coronary Obstruction in Transcatheter Aortic Valve-in-Valve Implantation. Circulation: Cardiovascular Interventions, 2015, 8, .	1.4	202
27	Synchronization Using Remote-Scope Promotion. , 2015, , .		23
28	Toward GPUs being mainstream in analytic processing. , 2015, , .		17
29	gem5-gpu: A Heterogeneous CPU-GPU Simulator. IEEE Computer Architecture Letters, 2015, 14, 34-36.	1.0	161
30	Implications of Emerging 3D GPU Architecture on the Scan Primitive. SIGMOD Record, 2015, 44, 18-23.	0.7	8
31	Synchronization Using Remote-Scope Promotion. Computer Architecture News, 2015, 43, 73-86.	2.5	4
32	Skewed Compressed Caches. , 2014, , .		48
33	A comparative analysis of microarchitecture effects on CPU and GPU memory system behavior. , 2014, , .		30
34	Heterogeneous-race-free memory models. , 2014, , .		73
35	QuickRelease: A throughput-oriented approach to release consistency on GPUs. , 2014, , .		47
36	Supporting x86-64 address translation for 100s of GPU lanes. , 2014, , .		93

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37	Decoupled Compressed Cache: Exploiting Spatial Locality for Energy Optimization. IEEE Micro, 2014, 34, 91-99.	1.8	14
38	Heterogeneous system coherence for integrated CPU-GPU systems. , 2013, , .		79
39	Reuse-based online models for caches. Performance Evaluation Review, 2013, 41, 279-292.	0.4	8
40	Transcatheter Valve-In-Valve Implantation for Failed Balloon-Expandable Transcatheter Aortic Valves. JACC: Cardiovascular Interventions, 2012, 5, 571-577.	1.1	60
41	Calvin: Deterministic or not? Free will to choose. , 2011, , .		40
42	A Primer on Memory Consistency and Cache Coherence. Synthesis Lectures on Computer Architecture, 2011, 6, 1-212.	1.3	211
43	The gem5 simulator. Computer Architecture News, 2011, 39, 1-7.	2.5	3,750
44	WiDGET. Computer Architecture News, 2010, 38, 2-13.	2.5	12
45	Forwardflow. Computer Architecture News, 2010, 38, 14-25.	2.5	2
46	Role of Multislice Computed Tomography in Transcatheter Aortic Valve Replacement. American Journal of Cardiology, 2009, 103, 1295-1301.	0.7	172
47	TokenTM: Efficient Execution of Large Transactions with Hardware Transactional Memory. , 2008, , .		54
48	TokenTM. Computer Architecture News, 2008, 36, 127-138.	2.5	40
49	Performance pathologies in hardware transactional memory. Computer Architecture News, 2007, 35, 81-91.	2.5	17
50	Interactions Between Compression and Prefetching in Chip Multiprocessors. , 2007, , .		44
51	LogTM-SE: Decoupling Hardware Transactional Memory from Caches. , 2007, , .		261
52	ASR: Adaptive Selective Replication for CMP Caches. Microarchitecture (MICRO), Proceedings of the Annual International Symposium on, 2006, , .	0.0	169
53	Supporting nested transactional memory in logTM. Computer Architecture News, 2006, 34, 359-370.	2.5	0
54	Supporting nested transactional memory in logTM. , 2006, , .		111

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55	Supporting nested transactional memory in logTM. ACM SIGPLAN Notices, 2006, 41, 359-370.	0.2	12
56	Supporting nested transactional memory in logTM. Operating Systems Review (ACM), 2006, 40, 359-370.	1.5	16
57	Evaluating scheduling policies for fine-grain communication protocols on a cluster of SMPs. Journal of Parallel and Distributed Computing, 2005, 65, 464-478.	2.7	0
58	Multifacet's general execution-driven multiprocessor simulator (GEMS) toolset. Computer Architecture News, 2005, 33, 92-99.	2.5	1,250
59	Token coherence. , 2003, , .		168
60	Token coherence. Computer Architecture News, 2003, 31, 182-193.	2.5	30
61	Using destination-set prediction to improve the latency/bandwidth tradeoff in shared-memory multiprocessors. , 2003, , .		78
62	SafetyNet. Computer Architecture News, 2002, 30, 123-134.	2.5	105
63	Full-system timing-first simulation. , 2002, , .		63
64	Timestamp snooping. Operating Systems Review (ACM), 2000, 34, 25-36.	1.5	3
65	Timestamp snooping. Computer Architecture News, 2000, 28, 25-36.	2.5	19
66	Timestamp snooping. , 2000, , .		42
67	Multicast snooping. Computer Architecture News, 1999, 27, 294-304.	2.5	24
68	Analytic evaluation of shared-memory systems with ILP processors. Computer Architecture News, 1998, 26, 380-391.	2.5	12
69	Reactive NUMA. Computer Architecture News, 1997, 25, 229-240.	2.5	6
70	Relaxed consistency and coherence granularity in DSM systems. ACM SIGPLAN Notices, 1997, 32, 193-205.	0.2	6
71	Decoupled hardware support for distributed shared memory. Computer Architecture News, 1996, 24, 34-43.	2.5	3
72	Paging tradeoffs in distributed-shared-memory multiprocessors. Journal of Supercomputing, 1996, 10, 87-104.	2.4	13

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73	Decoupled hardware support for distributed shared memory. , 1996, , .		55
74	Coherent network interfaces for fine-grain communication. Computer Architecture News, 1996, 24, 247-258.	2.5	11
75	Active memory. Performance Evaluation Review, 1995, 23, 220-230.	0.4	5
76	Dynamic self-invalidation. , 1995, , .		93
77	Dynamic self-invalidation. Computer Architecture News, 1995, 23, 48-59.	2.5	15
78	Fine-grain access control for distributed shared memory. , 1994, , .		167
79	The Wisconsin Wind Tunnel project. Computer Architecture News, 1994, 22, 19-26.	2.5	2
80	Cost/performance of a parallel computer simulator. ACM SIGSIM Simulation Digest, 1994, 24, 173-182.	0.1	0
81	Mechanisms for cooperative shared memory. , 1993, , .		35
82	Cooperative shared memory. ACM Transactions on Computer Systems, 1993, 11, 300-318.	0.6	123
83	The Wisconsin Wind Tunnel. , 1993, , .		246
84	Mechanisms for cooperative shared memory. Computer Architecture News, 1993, 21, 156-167.	2.5	3
85	Cooperative shared memory. , 1992, , .		58
86	A model for estimating trace-sample miss ratios. , 1991, , .		97