

Yu Chen

List of Publications by Year in descending order

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5
papers

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2258059

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2272923

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129
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| # | ARTICLE | IF | CITATIONS |
|---|--|-----|-----------|
| 1 | Thermal Mitigation and Optimization Via Multitier Bond Wire Layout for IGBT Modules Considering Multicellular Electro-Thermal Effect. IEEE Transactions on Power Electronics, 2022, 37, 7299-7314. | 7.9 | 4 |
| 2 | Graph-Model-Based Generative Layout Optimization for Heterogeneous SiC Multichip Power Modules With Reduced and Balanced Parasitic Inductance. IEEE Transactions on Power Electronics, 2022, 37, 9298-9313. | 7.9 | 14 |
| 3 | A Layout Optimization Method to Reduce Commutation Inductance of Multi-Chip Power Module Based on Genetic Algorithm. , 2021, , . | | 3 |
| 4 | A Coupled-Inductor-Based LCC Resonant Converter With the Primary-Parallel-Secondary-Series Configuration to Achieve Output-Voltage Sharing for HV Generator Applications. IEEE Transactions on Power Electronics, 2019, 34, 6108-6122. | 7.9 | 26 |
| 5 | An SiC-Based Half-Bridge Module With an Improved Hybrid Packaging Method for High Power Density Applications. IEEE Transactions on Industrial Electronics, 2017, 64, 8980-8991. | 7.9 | 59 |