

Alessandro Trifiletti

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

70
papers

701
citations

16
h-index

24
g-index

77
ext. papers

867
ext. citations

2
avg, IF

3.94
L-index

#	Paper	IF	Citations
70	Leakage Power Analysis Attacks: A Novel Class of Attacks to Nanometer Cryptographic Circuits. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2010 , 57, 355-367	3.9	68
69	Three-Phase Dual-Rail Pre-charge Logic. <i>Lecture Notes in Computer Science</i> , 2006 , 232-241	0.9	51
68	Effectiveness of Leakage Power Analysis Attacks on DPA-Resistant Logic Styles Under Process Variations. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014 , 61, 429-442	3.9	36
67	A novel low-voltage low-power fully differential voltage and current gained CCII for floating impedance simulations. <i>Microelectronics Journal</i> , 2009 , 40, 20-25	1.8	34
66	88- μ S A 1-MHz Stray-Insensitive CMOS Current-Mode Interface IC for Differential Capacitive Sensors. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014 , 61, 1905-1916	3.9	29
65	Exploiting the Body of MOS Devices for High Performance Analog Design. <i>IEEE Circuits and Systems Magazine</i> , 2011 , 11, 8-23	3.2	29
64	Efficient Digital Background Calibration of Time-Interleaved Pipeline Analog-to-Digital Converters. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2012 , 59, 1373-1383	3.9	28
63	Linearization Technique for Source-Degenerated CMOS Differential Transconductors. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2007 , 54, 848-852		28
62	0.9-V CMOS cascode amplifier with body-driven gain boosting. <i>International Journal of Circuit Theory and Applications</i> , 2009 , 37, 193-202	2	25
61	Delay-Based Dual-Rail Precharge Logic. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2011 , 19, 1147-1153	2.6	24
60	Design Solutions for Sample-and-Hold Circuits in CMOS Nanometer Technologies. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2009 , 56, 459-463	3.5	24
59	High-Level Side-Channel Attack Modeling and Simulation for Security-Critical Systems on Chips. <i>IEEE Transactions on Dependable and Secure Computing</i> , 2008 , 5, 164-176	3.9	20
58	Low power DDA-based instrumentation amplifier for neural recording applications in 65 nm CMOS. <i>AEU - International Journal of Electronics and Communications</i> , 2018 , 92, 30-35	2.8	19
57	Analysis and Implementation of a Minimum-Supply Body-Biased CMOS Differential Amplifier Cell. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2009 , 17, 172-180	2.6	18
56	Improved Digital Background Calibration of Time-Interleaved Pipeline A/D Converters. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2013 , 60, 86-90	3.5	16
55	Analysis of data dependence of leakage current in CMOS cryptographic hardware 2007 ,		16
54	Streamline calibration modelling for a comprehensive design of ATI-based digitizers. <i>Measurement: Journal of the International Measurement Confederation</i> , 2018 , 125, 386-393	4.6	15

53	Avoiding the Gain-Bandwidth Trade Off in Feedback Amplifiers. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2011 , 58, 2108-2113	3.9	15
52	Comparative performance analysis and complementary triode based CMFB circuits for fully differential class AB symmetrical OTAs with low power consumption. <i>International Journal of Circuit Theory and Applications</i> , 2016 , 44, 1039-1054	2	15
51	Behavioral Modeling for Calibration of Pipeline Analog-To-Digital Converters. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2010 , 57, 1255-1264	3.9	14
50	0.6-V CMOS cascode OTA with complementary gate-driven gain-boosting and forward body bias. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 15-27	2	12
49	Design and validation through a frequency-based metric of a new countermeasure to protect nanometer ICs from side-channel attacks. <i>Journal of Cryptographic Engineering</i> , 2015 , 5, 269-288	1.9	11
48	. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 1504-1508	3.5	11
47	Template attacks exploiting static power and application to CMOS lightweight crypto-hardware. <i>International Journal of Circuit Theory and Applications</i> , 2017 , 45, 229-241	2	11
46	A Flip-Flop for the DPA Resistant Three-Phase Dual-Rail Pre-Charge Logic Family. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012 , 20, 2128-2132	2.6	11
45	Subsampling Models of Bandwidth Mismatch for Time-Interleaved Converter Calibration. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2015 , 62, 957-961	3.5	9
44	The Universal Circuit Simulator: A Mixed-Signal Approach to n -Port Network and Impedance Synthesis. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2007 , 54, 2178-2183	3.9	9
43	A new algorithm to extract the nonlinear model of MESFETS and HEMTS. <i>Microwave and Optical Technology Letters</i> , 1999 , 20, 297-302	1.2	9
42	Leakage Power Analysis attacks against a bit slice implementation of the Serpent block cipher 2014 ,		7
41	A new class-AB Flipped Voltage Follower using a common-gate auxiliary amplifier 2016 ,		6
40	Sub-1V CMOS OTA with Body-driven Gain Boosting 2007 ,		6
39	A synthesis-oriented approach to design stable circuits. <i>Microwave and Optical Technology Letters</i> , 1999 , 23, 354-357	1.2	6
38	An improved common-mode feedback loop for the differential-difference amplifier. <i>Analog Integrated Circuits and Signal Processing</i> , 2013 , 74, 33-48	1.2	5
37	Multivariate Analysis Exploiting Static Power on Nanoscale CMOS Circuits for Cryptographic Applications. <i>Lecture Notes in Computer Science</i> , 2017 , 79-94	0.9	4
36	Novel measurements setup for attacks exploiting static power using DC pico-ammeter 2017 ,		4

35	Leakage Power Analysis attacks: Effectiveness on DPA resistant logic styles under process variations 2011 ,		4
34	Stability Criterion for Two-Port Network With Input and Output Terminations Varying in Elliptic Regions. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2006 , 54, 4049-4055	4-1	4
33	CMOS Non-tailed differential pair. <i>International Journal of Circuit Theory and Applications</i> , 2016 , 44, 1468-1477		4
32	An improved reversed miller compensation technique for three-stage CMOS OTAs with double pole-zero cancellation and almost single-pole frequency response. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 1990-2005	2	3
31	Adaptive frequency compensation for maximum and constant bandwidth feedback amplifiers. <i>International Journal of Circuit Theory and Applications</i> , 2013 , 41, 424-440	2	3
30	Impact of Process Variations on LPA Attacks Effectiveness 2009 ,		3
29	Switched Capacitor Sample-and-Hold Circuit with Input Signal Range beyond Supply Voltage 2008 ,		3
28	A new model to analyze the effects of noise in a real oscillator. <i>Microwave and Optical Technology Letters</i> , 2002 , 32, 305-307	1.2	3
27	A low-power clock and data recovery circuit for 2.5 Gb/s SDH receivers 2000 ,		3
26	A shared memory, parameterized and configurable in FPGA, for use in multiprocessor systems 2016 ,		2
25	Near-optimum switched capacitor sample-and-hold circuit 2009 ,		2
24	A novel bias-dependent rational model for MESFET and HEMT devices. <i>Microwave and Optical Technology Letters</i> , 2000 , 24, 102-106	1.2	2
23	Monolithic 2.5-Gb/s clock and data recovery circuit based on silicon bipolar technology 1998 ,		2
22	A new design approach for monolithic transimpedance receivers based on root-locus techniques. <i>Microwave and Optical Technology Letters</i> , 1994 , 7, 692-696	1.2	2
21	Fully Differential Class-AB OTA with Improved CMRR. <i>Journal of Circuits, Systems and Computers</i> , 2017 , 26, 1750169	0.9	1
20	Autotuning technique for CMOS current mode capacitive sensor interfaces 2012 ,		1
19	A low-power sample-and-hold circuit based on a switched-opamp technique 2008 ,		1
18	A Sample-and-Hold Circuit with Very Low Gain Error for Time Interleaving Applications 2007 ,		1

17	Very Low Voltage CMOS Two-stage Amplifier 2007 ,		1
16	A Simple Technique for Fast Digital Background Calibration of A/D Converters. <i>Eurasip Journal on Advances in Signal Processing</i> , 2007 , 2008,	1.9	1
15	Input-Matching and Offset-Cancelling Networks for Limiting Amplifiers in Optical Communication Systems. <i>Analog Integrated Circuits and Signal Processing</i> , 2006 , 47, 23-32	1.2	1
14	Behavioral model of a noisy VCO for efficient time-domain simulation. <i>Microwave and Optical Technology Letters</i> , 2004 , 40, 352-355	1.2	1
13	A new procedure for nonlinear statistical model extraction of GaAs FET-integrated circuits. <i>International Journal of RF and Microwave Computer-Aided Engineering</i> , 2003 , 13, 348-356	1.5	1
12	A new instantaneous model of MESFET and HEMT devices for large-signal circuit design. <i>Microwave and Optical Technology Letters</i> , 2001 , 29, 187-190	1.2	1
11	Bias correction and yield optimization of MMICs with external digital control. <i>Microwave and Optical Technology Letters</i> , 2001 , 31, 134-137	1.2	1
10	A new topology for a transimpedance amplifier with postfabrication bandwidth adjustment. <i>Microwave and Optical Technology Letters</i> , 2000 , 25, 47-51	1.2	1
9	A new procedure for the extraction of a multibias linear model for MESFETs and HEMTs. <i>Microwave and Optical Technology Letters</i> , 2000 , 25, 263-266	1.2	1
8	Design criteria for optical receivers in broadband optical systems. <i>Microwave and Optical Technology Letters</i> , 1999 , 20, 50-53	1.2	1
7	A Novel 0.6V MCML D-Latch Topology exploiting Dynamic Body Bias Threshold Lowering 2018 ,		1
6	Design, realization, and test of a 900 MHz ceramic oscillator. <i>Microwave and Optical Technology Letters</i> , 2007 , 49, 1713-1717	1.2	0
5	Wideband LNA design by parallel FETs. <i>Microwave and Optical Technology Letters</i> , 2002 , 32, 280-284	1.2	0
4	Process and terminations variations aware stability criteria for microwave amplifiers. <i>International Journal of RF and Microwave Computer-Aided Engineering</i> , 2013 , 23, 619-626	1.5	
3	Extraction of CAD-compatible statistical nonlinear models of GaAs HEMT MMICs. <i>Microwave and Optical Technology Letters</i> , 2009 , 51, 2163-2166	1.2	
2	Design of narrowband amplifiers with conditionally stable transistors. <i>Microwave and Optical Technology Letters</i> , 2001 , 31, 208-210	1.2	
1	A New Fully Closed-Loop, High-Precision, Class-AB CCII for Differential Capacitive Sensor Interfaces. <i>Electronics (Switzerland)</i> , 2022 , 11, 903	2.6	