

# Akira Mochizuki

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11599262/publications.pdf>

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5  
papers

47  
citations

1937685

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2272923

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docs citations

5  
times ranked

46  
citing authors

#	ARTICLE	IF	CITATIONS
1	Challenge of MTJ-based nonvolatile logic-in-memory architecture for ultra low-power and highly dependable VLSI computing. , 2015, , .		2
2	Design of an energy-efficient 2T-2MTJ nonvolatile TCAM based on a parallel-serial-combined search scheme. IEICE Electronics Express, 2014, 11, 20131006-20131006.	0.8	9
3	Complementary 5T-4MTJ nonvolatile TCAM cell circuit with phase-selective parallel writing scheme. IEICE Electronics Express, 2014, 11, 20140297-20140297.	0.8	7
4	A compact low-power nonvolatile flip-flop using domain-wall-motion-device-based single-ended structure. IEICE Electronics Express, 2014, 11, 20140296-20140296.	0.8	7
5	Fabrication of a magnetic tunnel junction-based 240-tile nonvolatile field-programmable gate array chip skipping wasted write operations for greedy power-reduced logic applications. IEICE Electronics Express, 2013, 10, 20130772-20130772.	0.8	22