

Maximiliam Luppe

List of Publications by Year in descending order

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Version: 2024-02-01

13
papers

38
citations

2258059

3
h-index

2053705

5
g-index

13
all docs

13
docs citations

13
times ranked

40
citing authors

#	ARTICLE	IF	CITATIONS
1	Low-Latency Reconfigurable Entropy Digital True Random Number Generator With Bias Detection and Correction. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1562-1575.	5.4	18
2	Fractal dimension based on Minkowski-Bouligand method using exponential dilations. Electronics Letters, 2015, 51, 475-477.	1.0	6
3	Low-Noise Amplifier for Deep-Brain Stimulation (DBS). Electronics (Switzerland), 2022, 11, 939.	3.1	5
4	Parallel implementation of exact dilations and multi-scale skeletonization. Real Time Imaging, 2003, 9, 163-169.	1.6	3
5	Weed segmentation in sugarcane crops using Mask R-CNN through aerial images. , 2020, , .		2
6	Design of a low power 10-bit 12MS/s asynchronous SAR ADC in 65nm CMOS. , 2019, , .		1
7	FPGA Implementation of a Pseudorandom Number Generator Based on $k \hat{=}$ " Logistic Map. , 2020, , .		1
8	Restructuring the Digital Systems Laboratory in Computer Engineering Course. Revista Eletrônica Engenharia Viva, 2018, 5, 51.	0.0	1
9	Charge-Pump Circuit in 65nm CMOS for Neural Stimulation on Deep-Brain Stimulation. , 2021, , .		1
10	Architecture for fractal dimension estimation based on Minkowski-Bouligand method using integer distances. , 2016, , .		0
11	Aerial image mosaics built using images with vegetation index pre-calculated. Proceedings of SPIE, 2016, , .	0.8	0
12	Performance Comparison of High-Speed Dual Modulus Prescalers Using Metaheuristic Sizing/Optimization. , 2020, , .		0
13	A low-power 10-bit 0.01-to-12-MS/s asynchronous SAR ADC in 65-nm CMOS. Analog Integrated Circuits and Signal Processing, 2021, 106, 321-337.	1.4	0