## Patrick Sheridan

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/11570950/publications.pdf

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		1163117	1372567	
16	2,254	8	10	
papers	citations	h-index	g-index	
17	17	17	2586	
all docs	docs citations	times ranked	citing authors	

#	Article	IF	CITATIONS
1	Experimental Demonstration of a Second-Order Memristor and Its Ability to Biorealistically Implement Synaptic Plasticity. Nano Letters, 2015, 15, 2203-2211.	9.1	473
2	Stochastic memristive devices for computing and neuromorphic applications. Nanoscale, 2013, 5, 5872.	5.6	395
3	Biorealistic Implementation of Synaptic Functions with Oxide Memristors through Internal Ionic Dynamics. Advanced Functional Materials, 2015, 25, 4290-4299.	14.9	360
4	Synaptic behaviors and modeling of a metal oxide memristive device. Applied Physics A: Materials Science and Processing, 2011, 102, 857-863.	2.3	355
5	Complementary resistive switching in tantalum oxide-based resistive memory devices. Applied Physics Letters, 2012, 100, .	3.3	192
6	Experimental Demonstration of Feature Extraction and Dimensionality Reduction Using Memristor Networks. Nano Letters, 2017, 17, 3113-3118.	9.1	158
7	Data Clustering using Memristor Networks. Scientific Reports, 2015, 5, 10492.	3.3	100
8	Device and SPICE modeling of RRAM devices. Nanoscale, 2011, 3, 3833.	5 <b>.</b> 6	84
9	Efficient in-memory computing architecture based on crossbar arrays. , 2015, , .		81
10	Pattern recognition with memristor networks. , 2014, , .		25
11	Modeling and implementation of oxide memristors for neuromorphic applications. , 2012, , .		9
12	Memristors and Memristive Devices for Neuromorphic Computing. , 2014, , 129-149.		8
13	3-D Vertical Dual-Layer Oxide Memristive Devices. IEEE Transactions on Electron Devices, 2014, 61, 2581-2583.	3.0	6
14	Defect considerations for robust sparse coding using memristor arrays., 2015,,.		4
15	Improvement of RRAM Device Performance Through On-Chip Resistors. Materials Research Society Symposia Proceedings, 2012, 1430, 149.	0.1	2
16	Memristors and Memristive Devices forÂNeuromorphic Computing. , 2019, , 369-389.		2