

Young-Hwa Kim

List of Publications by Year in descending order

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2258059

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179
citing authors

#	ARTICLE	IF	CITATIONS
1	A 1-GS/s 9-bit Zero-Crossing-Based Pipeline ADC Using a Resistor as a Current Source. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 2570-2579.	3.1	10
2	A Hybrid-Domain Two-Step Time-to-Digital Converter Using a Switch-Based Time-to-Voltage Converter and SAR ADC. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 631-635.	3.0	20
3	A 7 bit, 3.75 ps Resolution Two-Step Time-to-Digital Converter in 65 nm CMOS Using Pulse-Train Time Amplifier. IEEE Journal of Solid-State Circuits, 2013, 48, 1009-1017.	5.4	138
4	A time-domain flash ADC immune to voltage controlled delay line non-linearity. , 2011, , .		0
5	Time-interleaved single-slope ADC using counter-based time-to-digital converter. , 2011, , .		6
6	A 10-bit 300MSample/s pipelined ADC using time-interleaved SAR ADC for front-end stages. , 2010, , .		8
7	A time-based successive approximation register analog-to-digital converter using a pulse width modulation technique with a single capacitor. , 2009, , .		1