Ramon G Carvajal

List of Publications by Year in Descending Order

35 papers	333 citations	11 h-index	17 g-index
36 ext. papers	409 ext. citations	1.8 avg, IF	3.14 L-index

#	Paper	IF	Citations
35	Energy-Efficient Amplifiers Based on Quasi-Floating Gate Techniques. *Applied Sciences (Switzerland)*, **2021**, 11, 3271	2.6	0
34	1-V 15-μW 130-nm CMOS Super Class AB OTA **2020**,		1
33	Class AB amplifier with enhanced slew rate and GBW. *International Journal of Circuit Theory and Applications*, **2019**, 47, 1199	2	7
32	Enhanced Single-Stage Folded Cascode OTA Suitable for Large Capacitive Loads. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2018**, 65, 441-445	3.5	19
31	0.18-V supply voltage gate-driven PGA with 0.7-Hz to 2-kHz constant bandwidth and 0.15-μW power dissipation. *International Journal of Circuit Theory and Applications*, **2018**, 46, 272-279	2	7
30	Folded Cascode OTA with 5540 MHzpF/mA FoM **2018**,		2
29	On the Optimal Current Followers for Wide-Swing Current-Efficient Amplifiers **2018**,		4
28	Analysis and design of highly linear triode-mode based OTA and its application to a wide tunable Gm-C filter. *International Journal of Circuit Theory and Applications*, **2017**, 45, 1218-1230	2	3
27	Super class AB OTA without open-loop gain degradation based on dynamic cascode biasing. *International Journal of Circuit Theory and Applications*, **2017**, 45, 2111-2118	2	18
26	High current efficiency class-AB OTA with high open loop gain and enhanced bandwidth. *IEICE Electronics Express*, **2017**, 14, 20170719-20170719	0.5	8
25	Low-Power Analog Channel Selection Filtering Techniques. *Circuits, Systems, and Signal Processing*, **2017**, 36, 895-915	2.2	1
24	Free class AB–AB Miller opamp with high current enhancement. *Electronics Letters*, **2015**, 51, 215-217	1.1	10
23	Low-power CMOS variable gain amplifier based on a novel tunable transconductor. *IET Circuits, Devices and Systems*, **2015**, 9, 105-110	1.1	13
22	Highly accurate CMOS second generation current conveyor and transconductor **2015**,		2
21	Highly linear micropower class AB current mirrors using Quasi-Floating Gate transistors. *Microelectronics Journal*, **2014**, 45, 1261-1267	1.8	12
20	Highly linear wide-swing continuous tuning of CMOS transconductors. *International Journal of Circuit Theory and Applications*, **2014**, 42, 831-841	2	4
19	Improved technique for continuous tuning of CMOS transconductor **2013**,		2

18	Design of micropower class AB transconductors: A systematic approach. *Microelectronics Journal*, **2013**, 44, 920-929	1.8	8
17	. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2013**, 60, 1300-1309	3.9	26
16	The Flipped Voltage Follower: Theory and Applications. *Lecture Notes in Electrical Engineering*, **2013** , 269-287	0.2	4
15	Power-efficient analog design based on the class AB super source follower. *International Journal of Circuit Theory and Applications*, **2012**, 40, 1143-1163	2	28
14	Micropower high current-drive class AB CMOS current-feedback operational amplifier. *International Journal of Circuit Theory and Applications*, **2011**, 39, 893-903	2	14
13	Tunable rail-to-rail FGMOS transconductor **2010**,		3
12	Class AB CMOS tunable transconductor **2010**,		3
11	Highly Linear Tunable CMOS $Gm{hbox{-}}C$ Low-Pass Filter. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2009**, 56, 2145-2158	3.9	51
10	Linear-enhanced V to I converters based on MOS resistive source degeneration **2008**,		2
9	Class-AB Fully Differential Voltage Followers. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2008**, 55, 131-135	3.5	5
8	A power efficient and simple scheme for dynamically biasing cascode amplifiers and telescopic op-amps. *The Integration VLSI Journal*, **2008**, 41, 539-543	1.4	2
7	Class AB Pseudo-Differential CMOS Squarer Circuit **2007**,		2
6	A High-Swing, High-Speed CMOS WTA Using Differential Flipped Voltage Followers. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2007**, 54, 668-672	3.5	16
5	Winner-Take-All Class AB Input Stage. *Analog Integrated Circuits and Signal Processing*, **2006**, 46, 149-152	1.2	5
4	A proposal for high-performance CCII-based analogue CMOS design. *International Journal of Circuit Theory and Applications*, **2005**, 33, 379-391	2	14
3	Biasing CMOS amplifiers using MOS transistors in subthreshold region. *IEICE Electronics Express*, **2004**, 1, 339-345	0.5	30
2	Low-Voltage Analog Circuits Based on Wideband Capacitive Coupling. *Analog Integrated Circuits and Signal Processing*, **2003**, 37, 253-257	1.2	3
1	Very Low Voltage Rail-to-Rail Programmable-Gain CMOS Amplifier. *Analog Integrated Circuits and Signal Processing*, **2003**, 37, 269-273	1.2	4