## Ramon G Carvajal

## List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

35	333	11	17
papers	citations	h-index	g-index
36	409	1.8	3.14
ext. papers	ext. citations	avg, IF	L-index

#	Paper	IF	Citations
35	Highly Linear Tunable CMOS \$Gm{hbox{-}}C\$ Low-Pass Filter. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2009</b> , 56, 2145-2158	3.9	51
34	Biasing CMOS amplifiers using MOS transistors in subthreshold region. <i>IEICE Electronics Express</i> , <b>2004</b> , 1, 339-345	0.5	30
33	Power-efficient analog design based on the class AB super source follower. <i>International Journal of Circuit Theory and Applications</i> , <b>2012</b> , 40, 1143-1163	2	28
32	. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 1300-1309	3.9	26
31	Enhanced Single-Stage Folded Cascode OTA Suitable for Large Capacitive Loads. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2018</b> , 65, 441-445	3.5	19
30	Super class AB OTA without open-loop gain degradation based on dynamic cascode biasing. <i>International Journal of Circuit Theory and Applications</i> , <b>2017</b> , 45, 2111-2118	2	18
29	A High-Swing, High-Speed CMOS WTA Using Differential Flipped Voltage Followers. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2007</b> , 54, 668-672	3.5	16
28	Micropower high current-drive class AB CMOS current-feedback operational amplifier. <i>International Journal of Circuit Theory and Applications</i> , <b>2011</b> , 39, 893-903	2	14
27	A proposal for high-performance CCII-based analogue CMOS design. <i>International Journal of Circuit Theory and Applications</i> , <b>2005</b> , 33, 379-391	2	14
26	Low-power CMOS variable gain amplifier based on a novel tunable transconductor. <i>IET Circuits, Devices and Systems,</i> <b>2015</b> , 9, 105-110	1.1	13
25	Highly linear micropower class AB current mirrors using Quasi-Floating Gate transistors. <i>Microelectronics Journal</i> , <b>2014</b> , 45, 1261-1267	1.8	12
24	Free class ABAB Miller opamp with high current enhancement. <i>Electronics Letters</i> , <b>2015</b> , 51, 215-217	1.1	10
23	High current efficiency class-AB OTA with high open loop gain and enhanced bandwidth. <i>IEICE Electronics Express</i> , <b>2017</b> , 14, 20170719-20170719	0.5	8
22	Design of micropower class AB transconductors: A systematic approach. <i>Microelectronics Journal</i> , <b>2013</b> , 44, 920-929	1.8	8
21	Class AB amplifier with enhanced slew rate and GBW. <i>International Journal of Circuit Theory and Applications</i> , <b>2019</b> , 47, 1199	2	7
20	⊞0.18-V supply voltage gate-driven PGA with 0.7-Hz to 2-kHz constant bandwidth and 0.15-W power dissipation. <i>International Journal of Circuit Theory and Applications</i> , <b>2018</b> , 46, 272-279	2	7
19	Class-AB Fully Differential Voltage Followers. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2008</b> , 55, 131-135	3.5	5

18	Winner-Take-All Class AB Input Stage. Analog Integrated Circuits and Signal Processing, 2006, 46, 149-15	521.2	5
17	On the Optimal Current Followers for Wide-Swing Current-Efficient Amplifiers 2018,		4
16	Highly linear wide-swing continuous tuning of CMOS transconductors. <i>International Journal of Circuit Theory and Applications</i> , <b>2014</b> , 42, 831-841	2	4
15	Very Low Voltage Rail-to-Rail Programmable-Gain CMOS Amplifier. <i>Analog Integrated Circuits and Signal Processing</i> , <b>2003</b> , 37, 269-273	1.2	4
14	The Flipped Voltage Follower: Theory and Applications. <i>Lecture Notes in Electrical Engineering</i> , <b>2013</b> , 269-287	0.2	4
13	Analysis and design of highly linear triode-mode based OTA and its application to a wide tunable Gm-C filter. <i>International Journal of Circuit Theory and Applications</i> , <b>2017</b> , 45, 1218-1230	2	3
12	Tunable rail-to-rail FGMOS transconductor <b>2010</b> ,		3
11	Class AB CMOS tunable transconductor <b>2010</b> ,		3
10	Low-Voltage Analog Circuits Based on Wideband Capacitive Coupling. <i>Analog Integrated Circuits and Signal Processing</i> , <b>2003</b> , 37, 253-257	1.2	3
9	Folded Cascode OTA with 5540 MHzpF/mA FoM <b>2018</b> ,		2
8	Improved technique for continuous tuning of CMOS transconductor 2013,		2
7	Highly accurate CMOS second generation current conveyor and transconductor 2015,		2
6	Linear-enhanced V to I converters based on MOS resistive source degeneration 2008,		2
5	A power efficient and simple scheme for dynamically biasing cascode amplifiers and telescopic op-amps. <i>The Integration VLSI Journal</i> , <b>2008</b> , 41, 539-543	1.4	2
4	Class AB Pseudo-Differential CMOS Squarer Circuit <b>2007</b> ,		2
3	Low-Power Analog Channel Selection Filtering Techniques. <i>Circuits, Systems, and Signal Processing</i> , <b>2017</b> , 36, 895-915	2.2	1
2	1-V 15-IW 130-nm CMOS Super Class AB OTA <b>2020</b> ,		1
1	Energy-Efficient Amplifiers Based on Quasi-Floating Gate Techniques. <i>Applied Sciences (Switzerland)</i> , <b>2021</b> , 11, 3271	2.6	O