

# Sreela Sasi

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11505809/publications.pdf>

Version: 2024-02-01

24  
papers

142  
citations

2682572

2  
h-index

2917675

2  
g-index

24  
all docs

24  
docs citations

24  
times ranked

130  
citing authors

#	ARTICLE	IF	CITATIONS
1	Performance Evaluation of Proof-of-Work and Collatz Conjecture Consensus Algorithms. , 2019, , .		9
2	Seasonal Trend Analysis on Multi-Variate Time Series Data. , 2018, , .		6
3	Two-level text summarization from online news sources with sentiment analysis. , 2017, , .		8
4	Movie review summarization and sentiment analysis using rapidminer. , 2017, , .		12
5	Structured data extraction from emails. , 2017, , .		1
6	Object addition, removal and video authenticity testing. , 2016, , .		1
7	Sentiment Analysis of ISIS Related Tweets Using Absolute Location. , 2016, , .		17
8	Stroke in-patients' transfer to the ICU using ensemble based model. , 2016, , .		6
9	Tweeple's microblogs on illegal immigration in USA. , 2016, , .		0
10	Proper imputation techniques for missing values in data sets. , 2016, , .		41
11	Tree-based ensemble models for predicting the ICU transfer of stroke in-patients. , 2016, , .		3
12	Intelligent imputation technique for missing values. , 2016, , .		2
13	Predictive model for transferring stroke in-patients to Intensive Care Unit. , 2015, , .		4
14	Detection of Abnormal behavior in Dynamic Crowded Gatherings. , 2013, , .		6
15	Domain and range identifier module for semantic web search engines. , 2012, , .		3
16	Modified Fuzzy C-means Clustering Algorithm with Spatial Distance to Cluster Center of Gravity. , 2010, , .		4
17	Building Customized Cushion Seats Using Stereo Visions for Disabled. , 2010, , .		1
18	Autonomic University Administration System. , 2010, , .		0

#	ARTICLE	IF	CITATIONS
19	High performance, low latency double digit decimal multiplier on ASIC and FPGA. , 2009, , .		4
20	A novel Sigma- $\Delta$ based parallel analogue-to-residue converter. International Journal of Electronics, 2009, 96, 571-583.	1.4	3
21	Performance analysis of double digit decimal multiplier on various FPGA logic families. , 2009, , .		5
22	Dual-mode RNS based programmable decimation filter for WCDMA and WLANa. , 2008, , .		2
23	RNS Based Programmable Multi-Mode Decimation Filter for WCDMA and WiMAX. IEEE Vehicular Technology Conference, 2008, , .	0.4	4
24	Transients in CMOS Circuits. IETE Journal of Research, 1990, 36, 306-312.	2.6	0