

Sahil Shah

List of Publications by Year in descending order

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Version: 2024-02-01

20
papers

256
citations

1040056

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h-index

1199594

12
g-index

20
all docs

20
docs citations

20
times ranked

168
citing authors

#	ARTICLE	IF	CITATIONS
1	A Programmable and Configurable Mixed-Mode FPAA SoC. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, , 1-9.	3.1	90
2	Calibration of Floating-Gate SoC FPAA System. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 2649-2657.	3.1	23
3	Tuning of Multiple Parameters With a BIST System. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 1772-1780.	5.4	13
4	Models and Techniques for Temperature Robust Systems on a Reconfigurable Platform. Journal of Low Power Electronics and Applications, 2017, 7, 21.	2.0	12
5	SoC FPAA Hardware Implementation of a VMM+WTA Embedded Learning Classifier. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 28-37.	3.6	12
6	Remote System Setup Using Large-Scale Field Programmable Analog Arrays (FPAA) to Enabling Wide Accessibility of Configurable Devices. Journal of Low Power Electronics and Applications, 2016, 6, 14.	2.0	11
7	Reconfigurable analog classifier for knee-joint rehabilitation. , 2016, 2016, 4784-4787.		11
8	A Real-Time Vital-Sign Monitoring in the Physical Domain on a Mixed-Signal Reconfigurable Platform. IEEE Transactions on Biomedical Circuits and Systems, 2019, 13, 1690-1699.	4.0	11
9	A proof-of-concept classifier for acoustic signals from the knee joint on a FPAA. , 2016, , .		10
10	Transforming mixed-signal circuits class through SoC FPAA IC, PCB, and toolset. , 2016, , .		10
11	Low power speech detector on a FPAA. , 2017, , .		10
12	VMM + WTA Embedded Classifiers Learning Algorithm Implementable on SoC FPAA Devices. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 65-76.	3.6	10
13	Temperature Sensitivity and Compensation on a Reconfigurable Platform. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 604-607.	3.1	9
14	Security Implications for Ultra-Low Power Configurable SoC FPAA Embedded Systems. Journal of Low Power Electronics and Applications, 2018, 8, 17.	2.0	6
15	An SoC FPAA Based Programmable, Ladder-Filter Based, Linear-Phase Analog Filter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 592-602.	5.4	6
16	An approach to using RASP tools in analog systems education. , 2016, , .		4
17	CAD synthesis tools for floating-gate SoC FPAAs. Design Automation for Embedded Systems, 2021, 25, 161-176.	1.0	4
18	Proof-of-concept energy-efficient and real-time hemodynamic feature extraction from bioimpedance signals using a mixed-signal field programmable analog array. , 2017, , .		3

#	ARTICLE	IF	CITATIONS
19	Live demonstration: FPAA Demonstration Controlled through Android-Based Device. , 2016, , .		1
20	Special Session: Calibrating mismatch in an ISFET with a Floating-Gate. , 2022, , .		0